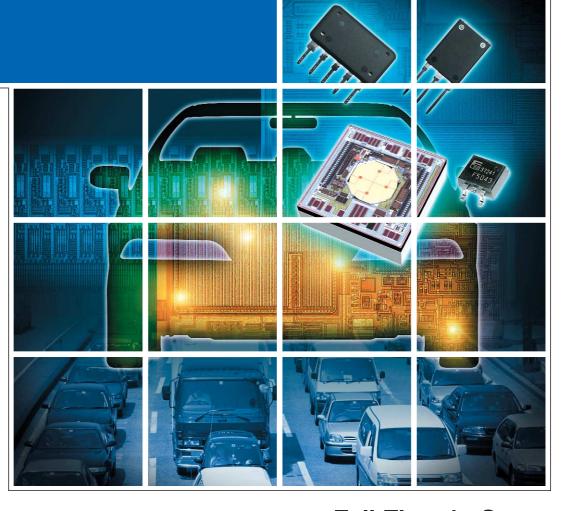
ISSN 0429-8284 Whole Number 205



# FUJI ELECTRIC REVIEW



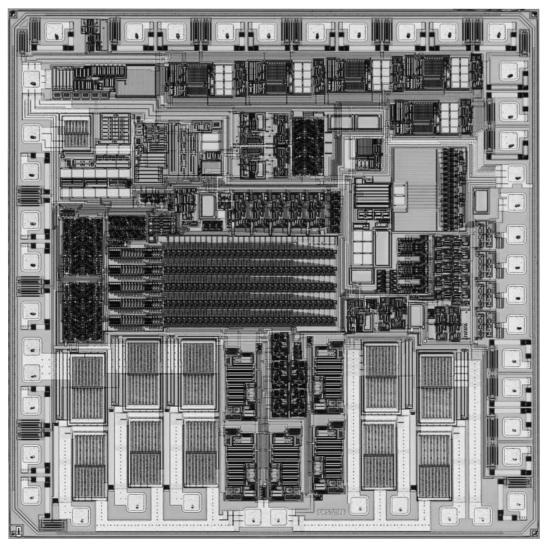
**Automotive Semiconductors** 



Fuji Electric Group

# Fuji Electric Power Supply ICs **Providing Multiple Solutions for Multiple Requirements**





# **Energy-saving Power Management Realized with a Single Chip**

FA7700V, FA7701V, FA7702P Examples: 1-channel

FA3686V, FA3687V, FA7703V, FA7704V, 2-channel

FA7715J

3-channel FA7711V

FA7708R, FA7716R 5-channel

6-channel FA3675F, FA3676F, FA7709R

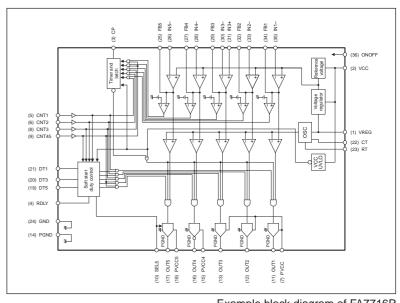
Uses: Power supplies for TFT panels, car audio systems, car

navigation systems, etc.

Features: ● The single chip solution integrating power transistors and control circuits by C/DMOS process capable of built-in low on-resistance DMOS output

transistors.

- Low power consumption by CMOS analog circuits.
- Wide range of applications for various power supply configurations such as synchronous rectification, switching polarity of drive transistors, etc.
- Various protection functions against overcurrent, overheat, short circuits, etc.
- Wide variety of packages meeting demands for smaller and thinner size. TSSOP-8, TSSOP-16, TSSOP-24, SON-16, QFN-36, VQFN-48, LQFP-48, etc.



Example block diagram of FA7716R





# **Automotive Semiconductors**

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# Cover photo:

Advances in electronics technology have led to remarkable improvements in the higher energy efficiency, reduced emissions of environmental pollutants, and increased safety, comfort and convenience of automobiles. The use of electronics technology in automobiles is expected to increase in the future.

Semiconductors such as sensors, microcomputers, memory, system LSI chips, analog ICs, power ICs and power devices are a core technology of car electronics. In response to requirements based on the severe environments in which they are used and safety, these devices must be high reliable.

Drawing on its distinctive power electronics-based technology, Fuji Electric has supplied outstanding high-reliability semiconductor products to many sectors of the automotive industry.

The cover photo shows several of Fuji Electric's representative automotive semiconductor products juxtaposed against the silhouette of an automobile as a representation of the progress in car electronics technology.

# Present Status and Future Prospects of Fuji Electric's Automotive Semiconductors

Tatsuhiko Fujihira Masaru Okumura

# 1. Introduction

Advanced by such powerful driving forces as initiatives to increase energy efficiency and reduce emissions in order to protect the global environment and user requirements for greater safety, comfort and convenience, electronics applications in automobiles have increased rapidly since the 1970s. Accordingly, there has been a dramatic increase in the types of semiconductors used in automobiles and their applications, and the usage of semiconductors per automobile has also continued to increase.

While increasing the usage of semiconductors, it is important that a low failure rate, spacious headroom and legroom, light weight, and low cost be maintained for the automobile as a whole. For this reason, higher reliability (lower failure rate), smaller size (smaller volume, smaller footprint and lighter weight) and lower cost are strongly required of automotive semiconductor products.

In response to requests from automobile manufacturers and automotive electrical equipment manufacturers, and to contribute to the higher energy efficiency, lower emissions, and enhanced safety, comfort and convenience of automobiles, Fuji Electric has developed and supplied many distinctive automotive semiconductor products and endeavored to realize greater reliability, smaller size and lower cost of those products.

Table 1 lists the automotive semiconductor products that Fuji Electric presently supplies or plans to supply in the future and their applications

For engine systems, Fuji Electric is presently supplying pressure sensors for manifold air pressure measurement and atmospheric pressure compensation, smart IGBTs and hybrid ICs as igniters for the ignition sub-system, high-voltage diodes to prevent premature ignition, MOSFETs for fuel injection-use and so on. In the future, Fuji Electric plans to supply smart MOSFETs and hybrid ICs for fuel injection-use, MOS-IPMs (intelligent power modules) for motors and generators, IGBT-IPMs for driving the motors of hybrid vehicles, integrated power ICs for use in electronic throttle valve control, in integrated control

sub-systems and in electronic control unit (ECU) power supplies, etc.

For chassis systems, Fuji Electric is presently supplying diodes, MOSFETs and smart MOSFETs for such applications as transmission control, traction control, brake control, suspension control, and power steering sub-systems in which the use of electronic control technology has advanced. Fuji Electric's future plans are to supply integrated power ICs for relatively low current applications, hybrid ICs and IPMs for relatively high current applications, and pressure sensors for oil pressure measurement.

As for body systems, Fuji Electric's MOSFETs and diodes are being used in power window, power lock, automated mirror, windshield wiper and other subsystems.

For other systems, Fuji Electric is providing diodes, MOS FETs, smart MOSFETs and the like for air conditioner, dome lamp, air bag, other lamps and ECU power supply sub-systems. In the future, Fuji Electric plans to supply power modules and pressure sensors for car air conditioner applications, an 80 V class of smart MOS FETs for dome lamp applications, and integrated power ICs for various other applications.

This paper will describe the current status and technological trends of Fuji Electric's representative automotive semiconductor products and then will discuss the future outlook for those products.

# 2. Automotive Diodes

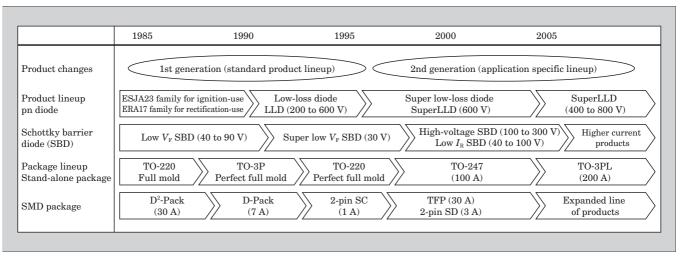
Fuji Electric's diodes are characterized by high reliability and low loss, and our product line has been expanded to include smaller size diodes for a wide range of automotive applications.

Figure 1 shows the roadmap of Fuji Electric's automotive diodes. Product development through 1997 was focused on supporting a wide range of applications and Fuji Electric's standard line of products included the ESJA23 family for ignition-use, the ERA17 family for rectification-use, low-loss diodes (LLDs), Schottky barrier diodes (SBDs) and so on. As of 1998, however, development pursued the goal of supplying application specific products having characteristics tailored for

Table 1 Fuji Electric's automotive semiconductor products and their applications

System	Sub-system	Diode	Power MOSFET	Smart MOSFET	Smart IGBT, IGBT, BJT	Power IC	Hybrid IC	IPM, Power module	Pressure sensor
	Engine control	0		0		0			0
	Ignition	0			0		0		
	Electronic fuel injection		0	0			0		
Engine	Electronic throttle valve control					0			
	Motor and generator		0				0	0	
	Motor drive for hybrid electric vehicle							0	
	Electronic transmission control	0	0	0		0			0
	Traction control	0	0	0		0			0
Chassis	Anti-lock braking	0	0	0		0			0
	Electronic suspension	0	0	0		0			
	Electronic power steering		0	0			0	0	
	Power window		0						
Body	Power lock		0						
Dody	Automated mirror	0	0						
	Windshield wiper		0						
	Air conditioning	0	0		0			0	0
	Dome lamp	0	0	0					
	Air bag		0	0					
Others	Headlight	0	0						
	Flasher lamp		0	0					
	Instrument panel lights			0		0			
	Power supply for ECUs	0	0			0			

Fig.1 Roadmap of Fuji Electric's automotive diodes



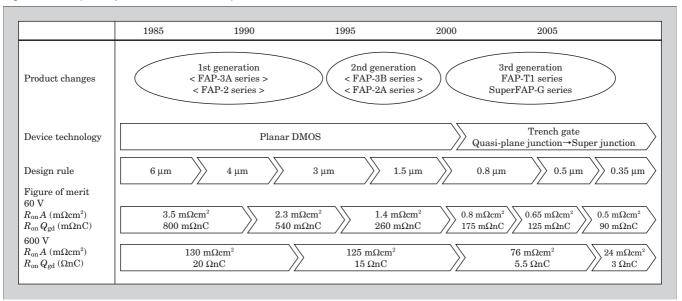
specified applications. Such application specific products included super low-loss diodes (super LLDs) and high voltage SBDs for DC-DC converters used in vehicles having multiple power supply lines such as hybrid vehicles, low reverse leakage current (low  $I_{\rm R}$ ) SBDs for applications requiring lower standby current and higher temperature environments and the like. The package lineup was also newly expanded to include the TO-247 package for higher current applica-

tions and the smaller and thinner 2-pin SC and 2-pin SD SMD packages, in addition to the standard axial-lead packages, the standard stand-alone TO packages (including full mold products) and SMDs (surface mounted devices).

# 3. Automotive Power MOSFETs

Benefiting from device technology innovation, typi-

Fig.2 Roadmap of Fuji Electric's automotive power MOSFETs



fied by trench-gate MOSFETs<sup>(1)</sup>, quasi-plane junction MOSFETs<sup>(2)</sup> and super junction MOSFETs<sup>(3)</sup>, and the unrelenting progress in semiconductor process technology as symbolized by shrinking design rules, there is no end in sight to the performance improvements for power MOSFETs.

Figure 2 shows the roadmap of Fuji Electric's automotive power MOSFETs. In contrast to the product development of diodes, which focused mainly on augmenting and expanding the line of application specific products, product development for power MOS FETs is a cyclic process in which previous generation products are repeatedly replaced by higher performance products of the next generation. From this trend, the dramatic rate of technological progress can be understood.

For low-voltage power MOSFETs, which are used in many applications such as power steering and air conditioning and whose range of applications is expected to expand in the future to include motors, generators and the like, the introduction of trench gate technology and the shrinking of design rules has achieved a reduction in specific ON-resistance to  $0.8~\mathrm{m}\Omega\mathrm{cm}^2$  in the case of  $60~\mathrm{V}$  mass-produced products, and to  $0.5~\mathrm{m}\Omega\mathrm{cm}^2$  in engineering samples. An approximate  $40~\mathrm{to}~50~\%$  decrease in ON-resistance every  $4~\mathrm{to}~5$  years has been an ongoing trend.

Meanwhile, the high-voltage power MOSFET used in such applications as DC-DC converters for hybrid electric vehicles and electronic ballast circuits for high-intensity discharge lamps has been improved in terms of reliability and ruggedness, but efforts to improve its performance have remained at an impasse for the past 10 years or so. The recently released SuperFAP-G series, however, uses the quasi-plane junction technology to realize an approximate 40 % decrease in specific ON-resistance and an approximate 50 % reduction in

switching time.

In the low-voltage range, finer line widths of trench-gate MOSFETs and in the high-voltage range, super-junction MOSFET technology, are expected to bring about future improvements to the performance of power MOSFETs.

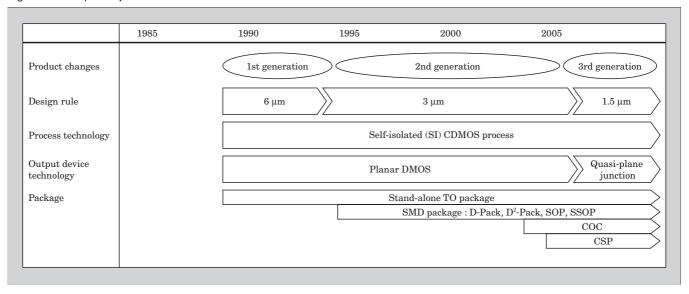
# 4. Automotive Smart MOSFETs

In the 1980s, power MOSFETs began to be used in automotive ECUs and the breakdown of power MOS FETs was scrutinized as one cause of ECU failure. The cause of breakdown was thought to be due to an excessive rise in temperature or the like brought about by such abnormal conditions as over-current caused by a short-circuit to the supply-line or ground in the lines leading from the outputs to the loads of the ECU, overvoltage caused by a load dump surge or the like, or a problem with the control software, and consequently it was requested that the power MOSFET be protected so as not to breakdown when these type of abnormal The smart MOSFET, a power conditions occur. MOSFET provided with built-in over-current, overvoltage and over-temperature protection functions, was developed in the latter half of the 1980s in response to this request.

Fuji Electric has responded by providing two lines of products, a low-cost smart MOSFET that is integrated with protection functions only, and an IPS (intelligent power switch) that is integrated with not only protection functions but also a power MOSFET drive circuit and diagnostic functions that are capable of detecting load abnormalities and notifying a microcomputer.

Figure 3 shows the roadmap of Fuji Electric's automotive smart MOSFETs. The process technology has consistently used a self-isolated CDMOS (comple-

Fig.3 Roadmap of Fuji Electric's automotive smart MOSFETs



mentary and double-diffused MOS) process, and the use of vertical power MOSFET in the output stage provides the characteristics of low ON-resistance and high ruggedness. The self-isolated CDMOS process is also advantageous in that enables lower cost manufacturing than the junction-isolation process used by many other companies.

Smart MOSFETs have been used widely in automotive applications ever since the 1990s, and with their increased usage per automobile, demands have grown stronger for smart MOSFETs that are smaller in size. In the future, low-current smart MOSFETs will trend toward using an integrated power IC process to integrate multiple channels into a single chip and to achieve smaller size, and smart MOSFETs themselves will become capable of handling higher voltage and higher current applications, but will require miniaturization for those applications. Fuji Electric plans to respond to these needs by advancing the development of 80 V products for applications that do not have a power Zener diode, chip-on-chip (COC) technology for high current applications, chip-size packages (CSPs) for achieving smaller size, and a 3rd generation process that provides the output MOSFET with a quasi-plane junction for achieving a smaller size chip.

# 5. Automotive Igniters

Electronic engine control and the increasing sophistication of that technology are said to have been the largest factors contributing to the higher fuel efficiency and lower emissions of automobiles since 1970. The precise control of air intake quantity, fuel injection timing and quantity, ignition timing and energy has enabled the realization of combustion closer to the ideal and has achieved improved fuel efficiency and lower emissions. The igniter is a key component that supplies electrical energy for ignition via an

ignition coil to the ignition plug.

Fuji Electric has been supplying transistors for igniters ever since the first half of the 1970s when igniters initially began to be provided with transistors. In 1978, Fuji succeeded in mass-producing an ignition hybrid IC that used thick-film circuit technology to integrate an igniter-use transistor and control power IC. Then in 1998, using self-isolated CDMOS process technology to integrate an ignition IGBT and control circuit into single chip, Fuji Electric began to mass-produce the world's only single-chip igniter with IGBT output.

Figure 4 shows the roadmap of Fuji Electric's single-chip igniter. The single-chip igniter is characterized by small size, high reliability and low cost and Fuji Electric has provided single-chip igniter products containing built-in current-limiting, voltage-clamping and waveform smoothing functions. In 2003, Fuji Electric began mass production of a new product containing a built-in function for over-temperature protection.

Most igniters of today are built-in to the ignition coil and attached directly to the engine. High surge-absorption capability, high reliability as typified by thermal cycling ruggedness, and high noise immunity such as immunity to EMI (electro-magnetic interruption) are required. Fuji Electric intends to use its accumulated technology and know-how to satisfy these requests while promoting the next generation process for more sophisticated single-chip igniters and working to make igniters smaller in size and at lower cost.

# 6. Automotive Pressure Sensors

The importance of engine control for improving fuel efficiency and reducing emissions has been discussed above, but pressure sensors are also critical for realizing those objectives and are an indispensable

Fig.4 Roadmap of Fuji Electric's single-chip igniter

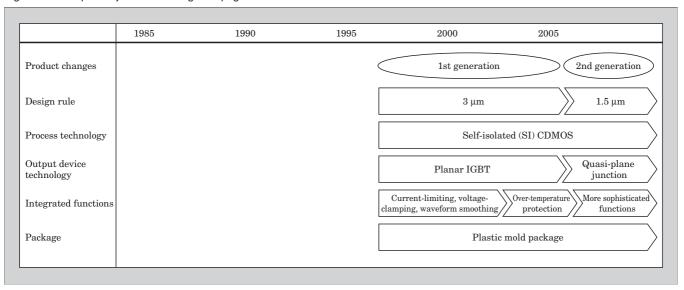
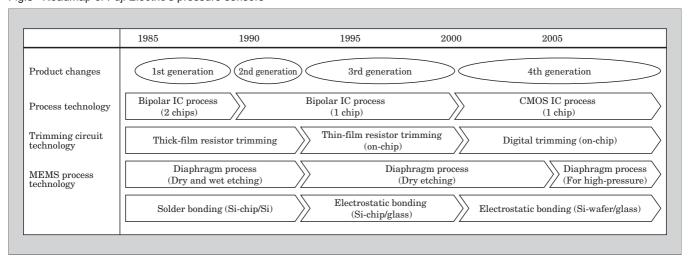


Fig.5 Roadmap of Fuji Electric's pressure sensors



semiconductor component for precisely controlling the amount of air intake.

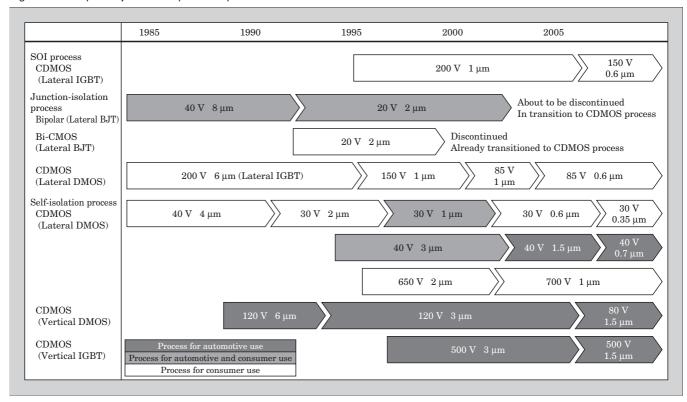
In 1984, Fuji Electric began to supply pressure sensors for engine control applications as mass-produced silicon diaphragm type sensors that used the piezo effect of a resistor. Because they are installed in environments of harsh temperature, heat, magnetic noise and the like, automotive pressure sensors are required to be highly reliable (and to retain their accuracy).

As can be seen from the pressure sensor roadmap of Fig. 5, Fuji Electric has addressed these requirements by promoting the development and improvement of high-precision concave processing technology, technology for adjusting temperature dependence and amplification linearity, sensor structures highly resistant to static electricity and magnetic noise, protection devices and the like. Moreover, in response to requests for lower cost, technology such as plastic-mold packages for replacing high-cost can packages, technology

for integrating the sensor and adjustment circuitry and the like onto a single chip, and digital trimming technology that utilizes an EPROM (electrically programmable ROM) instead of the thin-film trimming method have been developed and applied to commercial products.

The 4th generation pressure sensor, which has been mass produced since 2002, integrates a pressure transducer gauge resistor, CMOS amplifier circuit, digital adjusting circuit, EMI filter, surge protection device and the like into a single chip that is housed in a compact plastic mold package and achieves higher precision and twice the EMI immunity as 3rd generation products. Moreover, in the prior manufacturing method, a stress relaxation glass chip was bonded individually to each sensor chip, but Fuji Electric has developed and applied a technology for bonding electrostatically a glass plate directly to the "wafer-size" 6-inch silicon wafer on which ICs are fabricated and the application of this technology enables products to be

Fig.6 Roadmap of Fuji Electric's power IC process



made with smaller size, lighter weight, higher precision, higher reliability and lower cost.

At present, Fuji Electric is mass-producing automotive pressure sensors for use in engine control and motorcycle EFI (electronic fuel injection) systems. In addition to these low-pressure applications, 4th generation pressure sensor technology will be capable of expanding its range of applications to include high-pressure applications as well. In the future, Fuji Electric plans to apply high-pressure diaphragm technology, for which development has been completed, in order to provide a single-chip pressure sensor solution for such high-pressure applications as air conditioners, CVTs (continuously variable transmissions), brake oil pressure systems and the like.

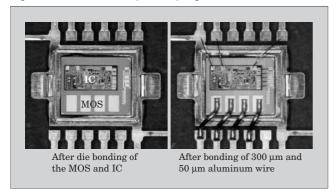
# 7. Future Outlook

The present status and trends of Fuji Electric's automotive semiconductor products have been discussed above. Awareness of the importance of protecting the global environment and requests for safety, comfort and convenience have increased year-by-year, and accordingly, the use of electronic components and systems in automobiles is expected to continue to accelerate. As the rate of semiconductor usage per automobile increases further, higher reliability, smaller size and lower price are requested of those automotive semiconductor products. Fuji Electric is moving steadily to develop technology that complies with those requests.

Figure 6 shows the roadmap of Fuji Electric's power IC process. As an overall trend, due to improvements in CMOS analog precision and operation frequency and in DMOS current drive capability, bipolar and Bi-CMOS processes have been discontinued and production is now concentrated on CDMOS process technology. The three isolation processes of an SOI (silicon on insulator) process for high-voltage multi-channel applications, a junction-isolation process for medium-voltage multi-channel application process for low-voltage multi-channel and high-voltage single-channel applications will be retained and used selectively according to the application.

One recent focus of automotive processes is a 40 V 1.5 µm self-isolated CDMOS process (having a lateral MOS output stage), which is an integrated power IC process for achieving smaller size of the ever-increasing power switch and peripheral circuitry by integrating multiple channels of low-current circuitry into a single chip. This 40 V 1.5 µm self-isolated CDMOS process is still in the start-up stage but mass production is slated to begin in 2004. The CDMOS process will become Fuji Electric's basic automotive power IC process, and in the future, micro-fabrication techniques will be advanced and established technology will be applied to develop this process into a vertical output self-isolated CDMOS process. One such example is an 80 V 1.5 µm CDMOS process (vertical MOS output) that will contribute to the smaller size and lower ONresistance of medium-current range smart MOSFETs, and another example is a 500 V 1.5 µm CDMOS

Fig.7 Internal view of chip-on-chip high-current smart MOSFET



process (vertical IGBT output) that will contribute to the higher performance of single-chip igniters.

In order to provide smart functionality to lower the failure rate of high-current power MOSFETs and to eliminate failures by using semiconductor-based relays, Fuji Electric is endeavoring to advance the development of COC smart MOSFET technology, an example of which is shown in Fig. 7, and plans to commercialize this technology in 2004. A 40 V 1.5  $\mu m$  CDMOS process is used to fabricate a control power IC on top of a low ON-resistance trench-gate MOSFET chip at the output stage, and the MOSFET and IC are connected by aluminum wire bonding to realize both small size and high reliability. Because these devices will be replaced by 2nd generation trench-gate MOSFET chips in the future, even lower ON-resistance and larger current capacity will be achieved.

Fuji Electric is also advancing the development of automotive IPMs for even larger current applications, which are mainly for the motor drive system of hybrid electric vehicles and fuel cell electric vehicles. By leveraging Fuji Electric's world-leading IGBT chip technology and industrial IPM technology, and by developing and adding new technology such as solder bonding technology, DCB (direct copper bonding) substrate technology, circuit technology for imbedding more sophisticated functions and the like to realize high reliability at a low cost as required for automotive applications, Fuji Electric plans to begin mass production of automotive IPMs in 2005.

For automotive pressure sensors, there has been progress in the development of technology for high-pressure applications. Fuji Electric intends to expand its line of products for high-pressure applications and plans to support 2 MPa applications by 2005 and 20 MPa applications by 2008.

Restrictions on the use of hazardous substances

that will be enforced in Europe beginning in 2005 are hastening the adoption of measures to counteract environmental problems, and all components and materials used in semiconductor products, except for some high-temperature solder, are required to be leadfree. Fuji Electric is steadily preparing for compliance with this regulation. Our power module products such as IPMs already use lead-free solder below the silicon chip portion of the module, and we plan to complete the transition to lead-free solder in the outer pins, printed circuit board and the DCB substrate by Spring 2005. Discrete products and power IC products in axial-leaded and stand-alone packages have been shipped lead-free since the 1st half of 2003 and SMD discrete products have been shipped lead-free since the 2nd half of 2003. We are preparing to make our hybrid ICs and pressure sensors lead-free compliant as of 2004.

# 8. Conclusion

Sufficient caution and pre-verification is necessary when using these remarkably innovative semiconductor products in automotive applications. Fuji Electric has maintained the high quality of its designs by adopting a cross-functional team approach to its design and control procedure, in which both the quality assurance and production departments participate from the initial design stage in order to work toward a good quality design that is easy to manufacture while maintaining an appropriate process capacity, and by using quality-function-deployment, design FMEA (failure mode and effect analysis), process FMEA and other techniques to verify that a design is appropriate for a customer's particular usage environment and usage method. Fuji Electric will continue to provide quality leading-edge products, even for new applications, that satisfy user requirements with reliable technology and management based on careful design verification.

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# **Automotive Diodes**

Taketo Watashima Shoji Kitamura Hiroaki Furihata

# 1. Introduction

Environmental problems and the need to conserve energy are powerful factors that have advanced the development of electric vehicles and hybrid electric vehicles in the automotive field. In particular, because hybrid electric vehicles require only a supply of fuel and do not need special charging equipment (ecostations), the quantity of production and range of model types of hybrid vehicles have been increasing year-by-year. A hybrid vehicle is equipped with, in addition to an engine, a motor, a high-voltage highpower battery to drive that motor, and a DC-DC converter to convert the voltage from a main battery and to supply that voltage as a low voltage source to conventional electronic autoparts. It is important that DC-DC converters for hybrid electric vehicles have high efficiency, small size and high reliability, and because DC-DC converters that handle large currents are likely to become a source of noise, it is also important that consideration be given to anti-noise capability.

This paper introduces Fuji Electric's high-voltage, low loss and low noise product line of diodes that have been developed for use in DC-DC converters in accordance with the growing use of electronic control units (ECUs) due to the increasing use of electronic components and systems in automobiles, and also introduces a high-voltage highly reliable diode that is being used in distributorless ignition systems (DLIS), an increasingly popular form of electronic ignition systems.

# 2. High-voltage SBD

# 2.1 Overview

Fuji Electric's newly developed Schottky barrier diode (SBD) is considered to be the ideal diode for power supply rectification and especially well suited for high voltage output rectification. Low-voltage SBDs (30 V and 45 V) are being used in 3.3 V and 5 V low-voltage output circuits, and high-voltage LLDs (low loss fast recovery diodes) (200 V, 300 V and 400 V) have been used in 12 V and higher high-voltage output circuits. In order to support requests for larger

capacity, smaller size and lower noise of 12 V and higher high-voltage outputs, reduction of the generated loss by improvement of the forward voltage  $(V_F)$  and reduction of the generated surge voltage and switching noise by improvement of the reverse recovery characteristic are required of diodes used in power supply rectification applications. An analysis of the loss occurring in a 12 V output stage diode in a power supply (250 W) that uses a 200 V LLD reveals that at least 90 % of loss is due to  $V_{\rm F}$ . Moreover, to suppress the surge voltage applied to a diode during switching and to suppress the noise generated by the steep dv/dtcharacteristic, additional components such as snubber circuits and EMI-suppressing beads have been used, but doing so increases the part count and leads to higher cost.

The LLDs used previously were pn junction diodes and there was a limit to the extent which their  $V_{\rm F}$  could be lowered. Also, there was a general tradeoff relation (reverse correlation) between soft recovery characteristics and  $V_{\rm F}$ , and it was extremely difficult to realize both low  $V_{\rm F}$  and soft recovery. Therefore, in recognition of the low  $V_{\rm F}$  and soft recovery characteristics of SBDs, by using a high-voltage SBD instead of the high-speed pn diode that had conventionally been used in high-voltage output circuits, lower loss and lower noise due to the soft recovery characteristics could be achieved simultaneously. Accordingly, the new high-voltage SBD targeted output stage applications ranging from 12 V to 48 V and, in contrast to the existing high-speed pn diode, was developed to:

- (1) ensure lower  $V_F$  characteristics,
- (2) ensure a soft recovery, and
- (3) have a  $V_{\text{RRM}}$  (or working voltage) of 120 to 250 V.

# 2.2 Chip design

(1) Chip edge design

Figure 1 shows the structure of a high-voltage SBD chip. The chip edge design utilizes a guard ring process. The breakdown voltage of the device is determined by the resistivity  $\rho$  and thickness t of the epitaxial layer (n-layer). Figure 2 shows the dependency of breakdown voltage  $(V_{\rm BR})$  on resistivity  $\rho$  and thickness t. Higher resistivity  $\rho$  and a greater

thickness *t* of the epitaxial layer were designed to achieve a higher breakdown voltage. Furthermore, the desired working voltage was secured by optimizing the concentration and diffusion depth of the guard ring.

# (2) Selection of barrier metal

Based on the considerations of paragraph (1) above, in order to secure a working voltage range of 120 to 250 V, it was necessary to increase the resistivity and to achieve an epitaxial layer thickness of at least 10  $\mu$ m. Assuming the same type of unipolar operation as a low-voltage SBD,  $V_F$  would be expected

Fig.1 Cross-section of SBD chip structure

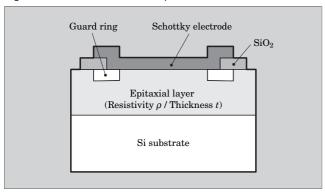
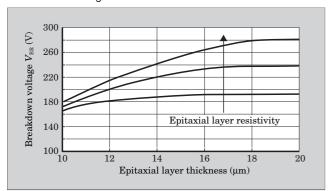


Fig.2 Relation between epitaxial layer thickness and breakdown voltage



to become considerably larger than that of a pn diode, however the injection of minority carriers (holes) from Schottky contacts and the guard ring acts to suppress  $V_{\rm F}$ . Below we shall verify how the selection of barrier metal changes the forward characteristics. Figure 3 shows the simulated results of the forward characteristics for three types of barrier metals a, b and c (having barrier heights of a<b<c) fabricated in 40 V, 150 V and 250 V epitaxial layers. For the 150 V and 250 V epitaxial layers, in the region of high current flow,  $V_{\rm F}$ decreased as barrier height increased (and the plotted curves intersected between 0.7 and 0.8 V). injection from the Schottky contacts increased as the resistivity and/or barrier height of the epitaxial layer increased. Figure 4 shows the relation between  $V_{\rm F}$  and  $I_{\rm R}$  ( $V_{\rm F}$ - $I_{\rm R}$  characteristics obtained by varying the barrier height for each voltage class) based on the results of Fig. 3. The 40 V class exhibits the usual  $V_{\rm F}$ - $I_{
m R}$  characteristic tradeoff, but at 150 V and 250 V,  $V_{
m F}$ decreases as barrier height increases. It is thought that the characteristics at 120 V are similar to those at 150 V. Moreover, in a 250 V SBD, a barrier height of metal b or higher is required to achieve a lower  $V_{\rm F}$ 

Fig.4 Relation between forward voltage and reverse current

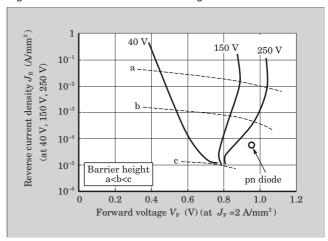


Fig.3 Simulated forward characteristics

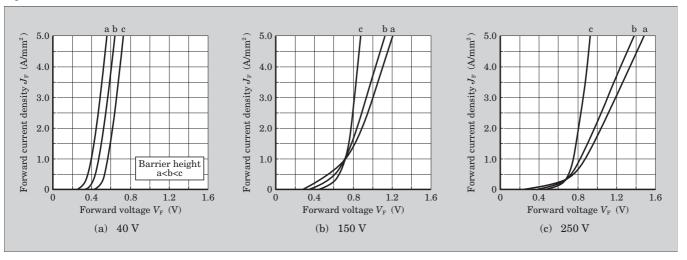


Fig.5 Forward and reverse characteristics of 120 V SBD (trial product)

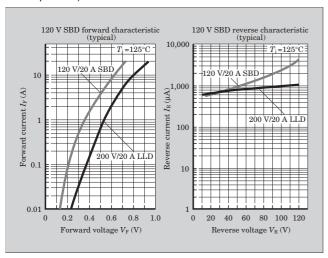
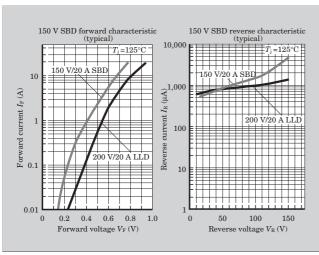


Fig.6 Forward and reverse characteristics of 150 V SBD (trial product)



characteristic than that of a pn diode.

# 2.3 Electrical characteristics

Based on the above considerations, we manufactured 120 V, 150 V and 250 V SBDs (having a rated current of 10 A). Figure 5 shows the forward and reverse characteristics of the 120 V SBD and Fig. 6 shows the forward and reverse characteristics of the 150 V SBD (where  $T_{\rm j}=125^{\circ}{\rm C}$ ). Fuji Electric's 200 V LLD is shown for comparison. Both the 120 V and 150 V SBDs have lower  $V_{\rm F}$  than the LLD. The low  $V_{\rm F}$  is particularly noticeable in the low current region. Figure 7 shows a comparison of the reverse characteristics of a 150 V SBD and a 200 V LLD. It can be seen that the SBD has somewhat lower reverse peak current ( $I_{\rm RP}$ ) and has a softer recovery.

# 2.4 Actual circuit test results

Figure 8 shows a comparison of the diode waveforms in a  $250\,\mathrm{W}$  12 V output power supply test circuit

Fig.7 Reverse recovery characteristic (trial product)

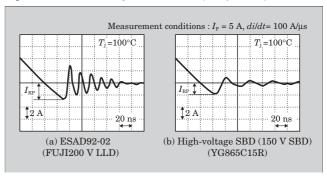


Fig.8 V and I waveforms of diode operation in 12 V output power supply circuit

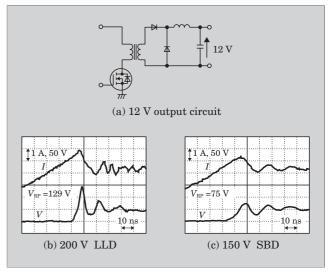
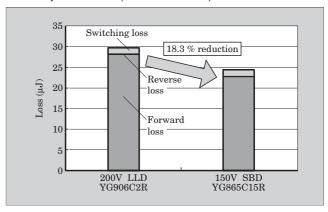


Fig.9 Loss comparison of 12 V output power supply secondary side diodes (simulated results)



using a 200 V LLD and also in the case of using a 150 V SBD. Figure 8(a) shows the evaluation circuit, and Figs. 8(b) and (c) show the forward waveforms of the diode. It can be seen that the SBD dramatically reduces surge voltage. A comparison of the loss calculated at the secondary-side diode in each of these cases is shown in Fig. 9. The SBD achieves an 18.3 % reduction in loss. 24 V and 48 V power supplies are expected to have similar results and an approximate

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Table 1 High-voltage SBD product line

		1	Maximum rating	ŗ	Electrical ch	aracteristic
Model number	Package	V <sub>RRM</sub> (V)	I <sub>O</sub> (A)	$I_{\mathrm{FSM}}$ (A)	$V_{\mathrm{FM}}(\mathrm{V}) \\ I_{\mathrm{FM}} = 0.5 \times I_{\mathrm{O}} \\ (T_{\mathrm{j}} = 25^{\circ}\mathrm{C})$	$I_{ m RRM} \ (\mu { m A}) \ V_{ m R} = V_{ m RRM}$
YA862C12R	TO-220	120	10	75	0.88	150
YG862C12R	TO-220F	120	10	75	0.88	150
TS862C12R	T-Pack	120	10	75	0.88	150
YA865C12R	TO-220	120	20	150	0.88	150
YG865C12R	TO-220F	120	20	150	0.88	150
TS865C12R	T-Pack	120	20	150	0.88	150
YA868C12R	TO-220	120	30	225	0.88	200
YG868C12R	TO-220F	120	30	225	0.88	200
TS868C12R	T-Pack	120	30	225	0.88	200
YA862C15R	TO-220	150	10	75	0.90	150
YG862C15R	TO-220F	150	10	75	0.90	150
TS862C15R	T-Pack	150	10	75	0.90	150
YA865C15R	TO-220	150	20	150	0.90	150
YG865C15R	TO-220F	150	20	150	0.90	150
PH865C15	TO-247	150	20	150	0.90	150
TS865C15R	T-Pack	150	20	150	0.90	150
YA868C15R	TO-220	150	30	225	0.90	200
YG868C15R	TO-220F	150	30	225	0.90	200
PH868C15	TO-247	150	30	225	0.90	200
TS868C15R	T-Pack	150	30	225	0.90	200
YA862C25R	TO-220	250	10	75	1.08	150
YG862C25R	TO-220F	250	10	75	1.08	150
TS862C25R	T-Pack	250	10	75	1.08	150
YA865C25R	TO-220	250	20	150	1.08	150
YG865C25R	TO-220F	250	20	150	1.08	150
PH865C25	TO-247	250	20	150	1.08	150
TS865C25R	T-Pack	250	20	150	1.08	150
YA868C25R	TO-220	250	30	225	1.08	200
YG868C25R	TO-220F	250	30	225	1.08	200
PH868C25	TO-247	250	30	225	1.08	200
TS868C25R	T-Pack	250	30	225	1.08	200

20 to  $30\,\%$  reduction in loss is anticipated.

# 2.5 Product lineup

Table 1 shows the high-voltage SBD product lineup.  $I_0$  ratings are 10 A, 20 A and 30 A and available packages are the TO-220, TO-220F, TO-247 and T-Pack (SMD).

# 3. High-voltage Diodes for DLIS-use

# 3.1 Overview

DLIS is a highly efficient system for electrically delivering a high voltage to individual spark plugs based on control signals from an electronic control unit (ECU), and is used to overcome the following disadvantages of the conventional distributor ignition system (in which a mechanical contact point rotates to deliver a high voltage to each spark plug):

- (1) burnout and energy loss caused by sparks at the point of contact,
- (2) the difficulty of achieving precise control at high rotational speeds, and
- (3) the generation of electromagnetic noise and loss of ignition energy due to sparks at the point of contact and the use of a high tension ignition

Figure 10 shows a circuit diagram of the coil distributed independent spark-type DLIS, which is the mainstream DLIS, and also shows a diagram of the ignition system. Below, the high-voltage diodes used in this coil distributed independent spark-type DLIS will be described.

# 3.2 Role of the high-voltage diode in a DLIS system and future challenges

In order to achieve high output voltage at the

secondary coil, the abrupt change in magnetic flux in the coil caused by the turnoff of a primary-side ignition transistor is utilized. However, a voltage is naturally generated in the secondary coil while the transistor is in the on-state, and this voltage which is also applied to the spark plug may cause pre-sparking to occur at times other than the optimal sparking interval. A high-voltage diode may be used to prevent this on-state voltage and pre-sparking, however.

For this reason, a high-voltage diode is incorporated in the ignition coil close the spark plug in an engine block. In order to ensure reliability, it is important that the design has been made heat-resistant. It is also necessary to consider the surge voltage in the case of misfiring, and the provision of the device with surge-proof capability is an important challenge for the

Fig.10 Coil distributed spark-type DLIS

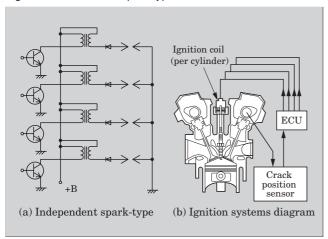
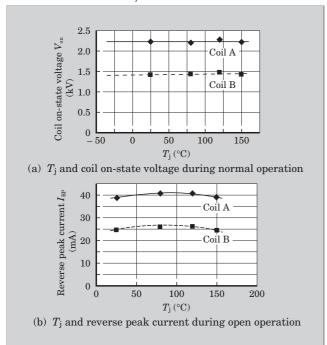


Fig.11 Relation between  $T_{\rm j}$  and  $V_{\rm on}$  at normal operation and relation between  $T_{\rm j}$  and  $I_{\rm RP}$  at abnormal (open) operation



future.

# 3.3 Device design

# (1) Breakdown voltage design

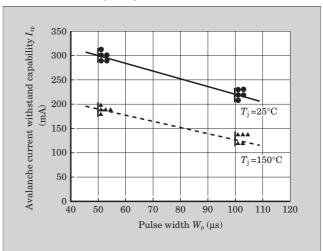
As an example of the results of an investigation of the voltage generated at the secondary side of the ignition coil during on-state operation of the transistor, Fig. 11(a) shows the relationship between diode junction temperature  $(T_{\rm j})$  and the voltage generated at the secondary side when  $I_{\rm c}=9$  A. Based on these findings, the actual voltage is assumed to be 2.5 kV or less.

Next, we simulated an abnormal operating condition in which the spark plug was assumed to be open-circuited and examined the electrical stress of an HVD (high-voltage diode). When the plug is open-circuited, a high reverse bias of several tens of kV is applied to the high-voltage diode and loss is generated due to the avalanche voltage and reverse current.

Figure 11(b) shows the relationship between the peak value of reverse current and  $T_{\rm j}$  during open operation. The pulse width of the reverse current was 100 µs or less. In the design stage, it has been proposed to make the  $V_{\rm RRM}$  (or working voltage) of an element higher than the voltage generated at the secondary coil during open plug operation, but this is impractical because a large voltage of several tens of kV would be applied to the entire system in the case of an abnormal operation. Additionally, a design that increases the breakdown voltage would lead to greater forward loss and the generation of heat, and as such, is not the best solution.

On the other hand, decreasing the breakdown voltage leads to lower loss, and in consideration of safety as well, the optimal design would be one in which the specified breakdown voltage is reduced to the extent possible and reverse surge withstand capability is ensured. Such a design is capable of achieving a drastic reduction in the heat generated by a high-voltage diode and is well suited for realizing a high

Fig.12 Relation between pulse width and avalanche current withstand capability



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Table 2 Absolute maximum ratings and electrical characteristics of EJA28-02S, ESJA28-03 and ESJA27-02S (a) Absolute maximum ratings

T4	Cl1		Rating		Unit	Condition
Item	Symbol	ESJA28-02S	ESJA28-03	ESJA27-02S	Unit	Condition
Repetitive peak reverse voltage	$V_{ m RM}$	2.2	2.7	2.2	$kV_{peak}$	Ignition pulse
Non-repetitive peak reverse voltage	$V_{ m RSM}$	2.5	3	2.5	kV <sub>peak</sub>	Ignition pulse
Average forward current (half sine-wave average)	$I_{\mathrm{o}}$	10	10	10	mA	f = 60  Hz, sine half-wave rectification
Non-repetitive peak forward current (10 ms)	$I_{ m surge}$	1	1	1	A <sub>peak</sub>	f = 60  Hz, sine-half wave, 1 cycle
Junction temperature	$T_{ m j}$	150	150	150	°C	_
Storage temperature	$T_{ m stg}$	- 40 to +150	-40 to +150	-40 to +150	°C	_
Package size	_	$\phi 2.5  imes 6.5$	$\phi 2.5  imes 6.5$	$\phi 2.5  imes 6.5$	mm	_

# (b) Electrical characteristics

Item	Cl1		Rating		Unit	Measurement condition
Item	Symbol	ESJA28-02S ESJA28-03 ESJA27-02S				(at $T_{\rm j} = 25^{\circ}{\rm C}$ )
Forward voltage	$V_{ m F}$	≤7	≤8.4	≤7	V	$I_{\mathrm{F}}$ = 10 mA
Reverse current	$I_{ m R1}$	≤5	≤5	≤5	μA	$-02S: V_R = 2.2 \text{ kV} \\ -03: V_R = 2.7 \text{ kV}$
Reverse current	$I_{ m R2}$	≤10	≤10	≤10	μA	$-02S: V_R = 2.5 \text{ kV}  -03: V_R = 3.0 \text{ kV}$
Avalanche breakdown voltage	$V_{ m av}$	2.7≤	3.3≤	2.7≤	kV	$I_{\rm av}$ = 100 $\mu { m A}$

heat-resistant design.

# (2) Design for reverse surge withstand capability

The ability to withstand reverse surges is necessary because of the sudden reverse voltage that exceeds the avalanche voltage and is applied during open plug operation. To achieve high surge withstand capability with a sufficient margin to withstand surge currents during open operation, this product incorporates such measures as:

- ① optimized chip resistivity, chip area and insulation layer thickness,
- 2 uniform silicon (Si) resistivity,
- 3 technology for achieving uniform p<sup>+</sup> and n<sup>+</sup> diffusion depths, and
- 4 technology for achieving uniform shape of the chip surface

and also reduces and equalizes the electric field intensity when an overvoltage is applied. Figure 12 shows the relationship between pulse width and avalanche current withstand capability.

# (3) Design for high temperature operation

The quality of materials, structure of the mold resin, chip surface passivation and the like are important factors for usage in high temperature environments such as this application and in usage environments where the temperature differential is large and causes high thermal stress. Accordingly, Fuji Electric has achieved a high-temperature-resistant design by implementing such measures as:

- ① assessing the degradation in material properties over time by testing the properties of resin materials in a high temperature storage test,
- 2 using the optimal external resin packaging based on heat shock tests, high temperature reverse bias tests and the like, and
- 3 adopting technology to ensure a proper and uniform thickness of the passivation layer.

# 3.4 Product introduction

Table 2 lists the maximum ratings and main electrical characteristics of Fuji Electric's DLIS prespark prevention high-voltage diode product line.

# 4. Conclusion

An overview of Fuji Electric's automotive diodes has been presented. Based on the products and technologies introduced herein, Fuji Electric intends to further expand its product line and is committed to advancing the development of even higher-grade products for the future.

# **Automotive Power MOSFETs**

Koji Horiuchi Yasuhiko Arita Takeyoshi Nishimura

# 1. Introduction

As the automobile industry has increasingly used electronic components and systems in recent years for the purpose of making car bodies that are lighter in weight and achieving better fuel efficiency, the use of electronic control units (ECUs) has advanced.

In particular, the transition from power steering systems that use conventional oil pressure control to DC motors that use electronic control (hereafter referred to as electric power steering ECUs) has been particularly rapid, and similarly, headlights are transitioning from conventional halogen bulbs to discharge bulbs that use electronically controlled ballast devices.

Additionally, environmental problems such as global warming have led to the commercialization of products one-after-another such as hybrid vehicles, electric vehicles and fuel-cell vehicles. In accordance with the trends in these automotive fields, there is strong demand for the improved performance of power MOSFETs which are used as the switching devices in ECUs.

Fuji Electric has responded to the trend of increased usage of electronic components and systems in automobiles by developing and commercializing various power MOSFETs.

This paper will introduce the product line, features and future development trends of Fuji Electric's automotive power MOSFETs.

# 2. Fuji Electric's Product Line of Automotive Power MOSFETs

Table 1 lists Fuji Electric's product line of automotive power MOSFETs and Fig. 1 shows the external appearance of those packages. As MOSFETs for electric power steering ECUs, Fuji provides a 60 V product line for 12 V battery-use and provides a partial lineup of 75 V products capable of supporting the upcoming changeover to 42 V power sources (36 V battery voltage) in the future. Fuji also has a line of 100 to 200 V MOSFETs for use in the DC-DC converter of hybrid vehicles and a line of 500 to 600 V MOSFETs for use in the electronic ballast circuits for discharge

bulbs.

# 3. Features of Fuji Electric's Automotive Power MOSFETs

# 3.1 Features of MOSFETs for electronic power steering systems

Electric power steering ECUs mainly use 60 V MOSFETs, but the upcoming changeover to a 42 V power source has led to requests for 75 V products.

These 60 V and 75 V MOSFET products utilize trench structure technology to realize lower ON-resistance and smaller package size. Features of the electric power steering MOSFETs are introduced below.

 Low ON-resistance chip structure and high gate reliability

Figure 2 shows a cross-sectional comparison of the conventional planar chip structure and the trench chip structure.

A characteristic of the trench chip structure is a concave structure fabricated at the gate by means of precision controlled etching. This structure enables the channel resistance component to be decreased, which had been difficult to achieve with the conventional planar chip construction, and also drastically reduces the resistance component due to a JFET effect. Figure 3 shows a comparison of the ON-resistance components of a 60 V conventional planar chip and a trench chip.

Fuji Electric has optimized the trench shape, uniform gate oxidation layer and the polysilicon layer that forms the gate electrode to achieve gate reliability capable of maintaining a high gate voltage ( $V_{\rm GS}$  = 30 V) simultaneously with low ON-resistance.

# (2) Optimization of the gate threshold voltage

The MOSFETs used in electric power steering ECUs are selected based on their low ON-resistance characteristics, and because the chip design involves a tradeoff between ON-resistance characteristics and gate threshold voltage, MOSFETs having a low gate threshold voltage of approximately 1 to 2 V are commonly used. However, a low gate threshold voltage is susceptible to malfunction caused by noise and the

Table 1 Fuji Electric's product line of automotive power MOSFETs

Tangeted application			Main pro	duct specifica	tion	
Targeted application such as ECUs	Model number	$V_{ m DSS} \ ({ m V})$	$I_{ m D}$ (A)	$R_{\mathrm{DS(on)}} \atop (\Omega)$	Package	Remark
	2SK3270-01	60	80	6.5 m	TO-220AB	
	2SK3271-01	60	100	6.5 m	TO-3P	
	2SK3272-01L, S	60	80	6.5 m	D <sup>2</sup> -Pack	
ECUs for electric	2SK3273-01MR	60	70	6.5 m	TO-220 full-mold	
power steering	F1519	60	80	6.0 m	D <sup>2</sup> -Pack	Under development
/	2SK3730-01MR	75	70	8.5 m	TO-220 full-mold	
	2SK3804-01S	75	70	8.5 m	D <sup>2</sup> -Pack	Under development
×	F1515	75	80	8.5 m	TO-247	Under development
/	2SK3644-01	100	30	44 m	TO-220AB	
\ /	2SK3645-01MR	100	30	44 m	TO-220 full-mold	
\ /	2SK3646-01L, S	100	30	44 m	D <sup>2</sup> -Pack	
Hybrid electric vehicles,	2SK3590-01	150	40	41 m	TO-220AB	
Electric vehicles, DC-DC converters,	2SK3591-01MR	150	40	41 m	TO-220 full-mold	
Electronic ballast for	2SK3592-01L, S	150	40	41 m	D <sup>2</sup> -Pack	
discharge bulbs (DC-DC converter/	2SK3594-01	200	30	66 m	TO-220AB	
\ inverter units)	2SK3595-01MR	200	30	66 m	TO-220 full-mold	
/	2SK3596-01L, S	200	30	66 m	D <sup>2</sup> -Pack	
/	2SK3504-01	500	14	0.46	TO-220AB	
	2SK3505-01MR	500	14	0.46	TO-220 full-mold	
	2SK3450-01	600	12	0.65	TO-220AB	
	2SK3451-01MR	600	12	0.65	TO-220 full-mold	

Fig.1 External appearance of packages

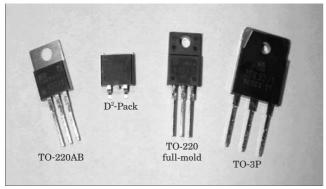


Fig.2 Planar chip structure and trench chip structure

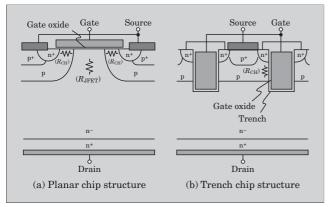
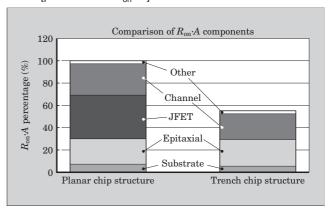


Fig.3 Comparison of the ON-resistance components of a 60 V conventional planar chip and a trench chip (60 V) [per unit area:  $R_{\rm on}$ · A]



like.

Fuji Electric's MOSFETs for electric power steering applications have an optimized gate threshold voltage of 3 V (typical value) and therefore the target circuitry can easily be configured to include anti-noise measures to prevent malfunction due to noise generated in the interconnects to gate peripheral circuitry.

(3) Highly reliable package compatible with large currents

An ECU system for electric power steering use as shown in Fig. 4 must have a highly reliable package capable of withstanding instantaneous large currents due to: 1) a load short-circuit of the DC motor, 2) a short-circuit to ground in the wiring harness that connects the DC motor and ECU, 3) an arm short-circuit between upper and lower devices that comprise of an H bridge or a 3-phase bridge, and so forth.

Figure 5 shows the internal structure of Fuji Electric's trench power MOSFET. When an electric power steering ECU operates at maximum torque (motor current of approximately 30 to 65 A), the large current flow causes power loss to occur in the chip and is dissipated as thermal energy and an even larger emission of thermal energy is dissipated in the lead wiring that connects to external pins. In consideration of the above problem, Fuji Electric has optimized the chip design by, (1) increasing the diameter of the internal connecting wire, and (2) by using multiple internal connecting wires.

Provided with the above features, Fuji Electric's electric power MOSFETs are being used in a wide range of applications in addition to those listed above.

(4) 75 V MOSFET product line

Although a battery voltage of 12 V is used at

Fig.4 Equivalent circuit of electric power steering ECU system (3-phase motor control)

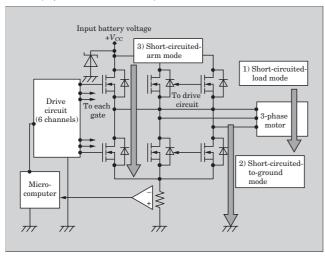
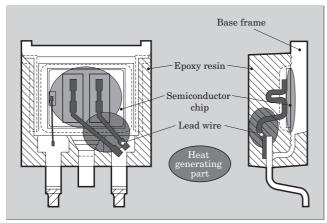


Fig.5 Internal structure of Fuji Electric's trench power MOSFET (surface mount type)



present, in order to support the future transition to a 42 V power source (36 V battery), Fuji Electric also provides a 75 V product line. Table 2 shows the main ratings and Table 3 shows the electrical characteristics of the 75 V product line. This product line is characterized by a high voltage of 75 V and by low ON-resistance (8.5 m $\Omega$  maximum).

# 3.2 Features of Fuji Electric's MOSFETs for use in DC-DC converters and electronic ballast circuits

Figure 6 shows a ballast device system for controlling a discharge bulb. 100 to 200 V MOSFETs are used in DC-DC converters to boost the battery voltage and 500 to 600 V MOSFETs are used in inverter units to generate a high voltage for a discharge bulb. A DC-DC converter requires a MOSFET capable of high-frequency switching operation in order to realize a compact and lightweight step-up transformer, and an inverter requires a high-voltage and high-speed MOSFET capable of withstanding the high voltage during the bulb unloaded output state of approximately 380 V and the high dv/dt at the beginning of bulb discharge. In response to these requests, Fuji Electric provides the SuperFAP-G product line which has high speed and low ON-resistance. This SuperFAP-G product line incorporates the new technology of a quasi-planar junction (QPJ) shown in Fig. 7 and achieves a high level of performance that is only 10 % less than the theoretical performance limit of silicon (Si).

Features of this product line are listed below (in comparison to the conventional 600 V product line.

- (1) 75 % decrease in turn-off loss
- (2) 60 % decrease in gate charge
- (3) High avalanche withstand capability
- (4) Package product line that includes various surface mount packages

By providing the above SuperFAP-G product line, Fuji Electric is able to supply MOSFETs that are ideally suited for DC-DC converter and electronic ballast applications.

Table 2 Main ratings of Fuji Electric's 75 V automotive power MOSFET ( $T_{\rm C} = 25^{\circ}{\rm C}$ )

Item	Symbol	Rating	Unit	Remark
Drain-source voltage	$V_{ m DS}$	75	V	
Drain source vortage	$V_{ m DSX}$	40	V	$V_{\mathrm{GS}}$ = -20 V
Continuous drain current	$I_{ m D}$	±70	A	
Pulsed drain current	$I_{ m DP}$	±280	A	
Gate-source voltage	$V_{ m GS}$	+30/-20	V	
Maximum avalanche energy	$E_{ m AV}$	443.8	mJ	$L$ = 84.5 $\mu$ H, $V_{\rm CC}$ = 48 V
Maximum power dissipation	$P_{ m D}$	162	w	
Operating temperature range	$T_{ m ch}$	175	$^{\circ}\mathrm{C}$	
Storage temperature range	$T_{ m stg}$	-55 to +175	°C	_

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Table 3 Electrical characteristics of Fuji Electric's 75 V automotive power MOSFET ( $T_{\rm C} = 25^{\circ}{\rm C}$  unless specified otherwise)

# (a) Static characteristics

Item	Symbol	Measur	ement	Star	ıdard	value	Unit
Item	Symbol	cond	ition	Min.	Тур.	Max.	Unit
Drain- source	$BV_{ m DSS}$	L D	$I_{\rm D} = 1 \text{ mA}$ $V_{\rm GS} = 0 \text{ V}$				V
breakdown voltage	$BV_{ m DSX}$	$I_{ m D}$ = $V_{ m GS}$ =		40	_	_	V
Gate threshold voltage	$V_{ m GS(th)}$	$I_{ m D}$ = 1 $V_{ m DS}$ =		2.5	3.0	3.5	V
Zero gate voltage	7	$V_{\mathrm{DS}}$ = 75 V	$T_{ m ch}$ =25°C	_	1.0	100	μΑ
drain current	$I_{ m DSS}$	$V_{\mathrm{GS}}$ = 0 V	$T_{ m ch}$ =125°C	_	10	500	μΑ
Gate-source leakage current	$I_{ m GSS}$	$V_{\mathrm{GS}}$ = + $V_{\mathrm{DS}}$ :		_	10	100	nA
Drain-source ON-state resistance	$R_{ m DS(on)}$	$I_{ m D}$ = $V_{ m GS}$ =		_	6.4	8.5	mΩ

# (b) Dynamic characteristics

T4	C1 -1	Measurement	Star	ndard v	value	TT '4
Item	Symbol	condition	Min.	Тур.	Max.	Unit
Forward transconductance	$g_{\mathrm{fs}}$	$I_{\rm D} = 35~{\rm A}$ $V_{\rm DS} = 10~{\rm V}$	25	50	-	S
Input capacitance	$C_{ m iss}$		_	7,500	_	
Output capacitance	$C_{ m oss}$	$V_{\mathrm{DS}} = 25 \mathrm{V}$ $V_{\mathrm{GS}} = 0 \mathrm{V}$	_	1,050	_	рF
Reverse transfer capacitance	$C_{ m rss}$	f = 1  MHz	_	500	_	P
Turn-on time	$t_{ m d(on)}$	$V_{cc} = 38 \text{ V}$	_	50	_	
Turn on onne	$t_{ m r}$	$V_{\rm GS} = 10 \text{ V}$	_	90	_	na
Turn-off time	$t_{ m d~(off)}$	$I_{\rm D}$ = 70 A $R_{\rm G}$ = 10 $\Omega$	_	150	-	ns
Turn on time	$t_{ m f}$	11G = 10 32	_	90	_	
Total gate charge	$Q_{ m g}$	$V_{cc} = 38 \text{ V}$	_	150	_	
Gate-source charge	$Q_{ m gs}$	$I_{\rm D} = 70~{\rm A}$	_	30	_	nC
Gate-drain charge	$Q_{ m gd}$	$V_{\mathrm{GS}}$ = 10 V	_	45	_	

# (c) Parasitic diode characteristics

Item	Symbol	Measurement	Star	Typ. Max.  A		Unit	
rtem	Symbol	condition	Min.	Min. Typ. Max.			
Avalanche withstand capability	$I_{ m AV}$	$L=84.5~\mu\mathrm{H}$ $T_\mathrm{ch}=25^{\circ}\mathrm{C}$	70	ı	_	A	
Diode forward ON-voltage	$V_{ m SD}$	$I_{\rm F} = 75~{\rm A}$ $V_{\rm GS} = 0~{\rm V}$ $T_{\rm ch} = 25^{\circ}{\rm C}$	_	1.3	1.65	V	
Reverse recovery time	$t_{ m rr}$	$I_{\rm F} = 35~{\rm A}$ $V_{\rm GS} = 0~{\rm V}$	_	95	_	ns	
Reverse recovery charge	$Q_{ m rr}$	$-di/dt = 100 \text{ A/}\mu\text{s}$ $T_{\text{ch}} = 25^{\circ}\text{C}$	_	0.30	_	μC	

# (d) Thermal characteristics

Item	Symbol	Measurement	Star	dard v	alue	Unit
rtem	Symbol	condition	ition   Min.   Typ.   Max		Max.	Onit
Thermal	$R_{ m th\ (ch-c)}$		_	_	0.926	°C/W
resistance	$R_{ m th\ (ch-a)}$		_	_	75.0	°C/W

Fig.6 Ballast device system

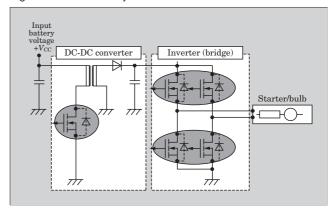
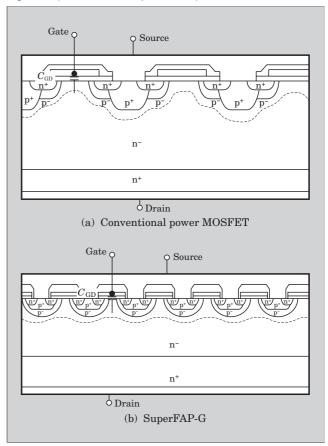


Fig.7 Chip structure of SuperFAP-G product line



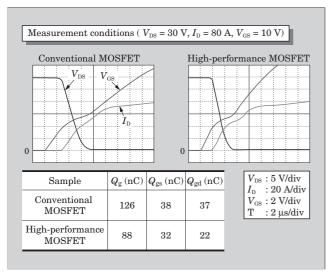
# 4. Future Development Trends

In addition to Fuji Electric's existing 60 V product line that has a track record of successful use in electric power steering application and the new 75 V product line, we are also endeavoring to develop high performance products that utilize next generation technology.

The design goals for these high performance products, for which development and commercialization being advanced, are listed below.

(1) Product voltage  $V_{\rm DS}$ : 60 V, 75 V

Fig.8 Comparison of switching characteristics (gate charge) of a conventional 60 V MOSFET and a new high-performance 60 V MOSFET



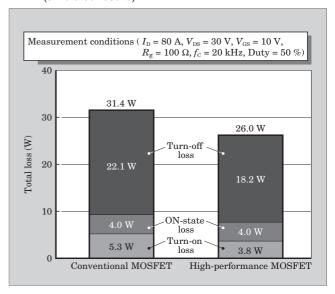
- (2) Rated current  $I_D$ : 70 to 80 A
- (3) Gate threshold voltage: 2.5 to 3.5 V
- (4) ON-resistance,  $R_{\rm on} \cdot A$ : 20 % less than conventional products
- (5) Input capacitance  $C_{\rm iss}$ : 30 % less than conventional products
- (6) Package lineup: Stand-alone packages (as typified by the TO-220) and surface mount packages

Figure 8 shows a comparison of the switching waveforms (gate charge) for a 60 V engineering sample of a product presently under development and a conventional MOSFET and Fig. 9 compares the results of a simulation of loss generation for these two devices assuming use as in an electric power steering application and a carrier frequency of 20 kHz.

According to these comparative results, an approximate 40 % improvement in gate charge quantity  $(Q_{\rm g}),$  effective in reducing loss during gate driving, and an approximate 18 % in loss generation at 20 kHz are achieved.

Based on the above results, by endeavoring to

Fig.9 Comparison of loss generated by a conventional 60 V MOSFET and a new high-performance 60 V MOSFET (simulated results)



improve the various main specifications and to enhance performance, higher performance can be achieved not only as a MOSFET for electric power steering applications, but by developing other product lines, higher performance can be achieved for additional applications as well.

# 5. Conclusion

Fuji Electric has developed and commercialized various automotive power MOSFETs including those described herein. We are committed to continue developing and providing distinctive products to satisfy the needs for electronic parts and systems and to expand the field of automotive electronics.

# Reference

(1) Yamazaki, T. et al. Low  $Q_{\rm gd}$  Trench Power MOSFETs with Robust Gate for Automotive Applications. PCIM2003.

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# **Automotive Smart MOSFETs**

Shin Kiuchi Minoru Nishio Takanori Kohama

# 1. Introduction

In the automotive electrical equipment industry, based on the goals of improving the environment, safety and comfort, electronic systems have grown in complexity in order to realize more advanced vehicle control technology and enhanced combustion technology for reducing gas emissions and increasing fuel efficiency, and these trends have led to increasingly sophisticated electronic control units (ECUs) yearafter-year. Furthermore, because the space for installation of an ECU is limited, the temperature of the environment in which ECUs operate has also been increasing year-by-year. Because of these circumstances, system manufacturers desire to make ECUs more compact in size and to increase their reliability in a high temperature environment. As semiconductor devices well suited for realizing small size and highly reliable ECUs, attention is focused on smart power devices that integrate a power semiconductor, peripheral protection circuits, a status output circuit, a drive circuit and the like into a single device. Applications of these smart power devices are steadily growing.

Fuji Electric has integrated power semiconductors and the abovementioned peripheral circuits into single chip solutions and has developed semiconductor products that are compatible with the smaller size, higher performance and higher reliability of ECUs. This product family includes high-side and low-side type intelligent power MOSFETs, IPSs (intelligent power switches) and single chip igniters. A common characteristic of these products is the integration of a power device with control circuitry, circuitry to protect against current, voltage and ESD (electrostatic discharge) surges, self-diagnosis circuitry and the like. This integration of electronic components into a single chip achieves lower cost and higher reliability than in the conventional case where the abovementioned circuits were added separately by system manufacturers. This paper introduces the intelligent power MOSFET and IPS which are typical smart MOSFETs and representative of the abovementioned semiconductor products.

# 2. Intelligent Power MOSFETs

# 2.1 Overview of product line

Table 1 lists Fuji Electric's product line of smart MOSFETs. The F5048 and F5045P have been newly added to the line of intelligent power MOSFET products. The F5048 is an 80 V product and has the advantage of eliminating the need for a 30 V power Zener diode that had conventionally been attached to the ECU to absorb the load dump surge (an excessive high energy surge of, for example 80 V for a period of  $\tau$  = 0.25 s, generated when the battery lead becomes open-circuited for some reason). The F5045P is the first high-side element in the intelligent power MOS FET product line. To enable operation directly from a battery power source, this product has a minimum operating voltage of 3 V and a standby current ( $I_{cc}$ ) of 90  $\mu$ A (typical value at  $T_i$  = 25°C).

As a representative device of the intelligent power MOSFET product line, main specifications of the F5041 are listed in Tables 2 and 3, and a circuit block diagram and chip die photo are shown in Figs. 1 and 2, respectively.

# 2.2 Characteristics

# (1) Short-circuit protection

Intelligent power MOSFETs contain a built-in short-circuit detection circuit to protect the system, load and device itself in case the load impedance in a system decreases and causes the current flow to become excessively large. As an example, Fig. 3 shows the operating waveform of the F5041 over the course of the sequence from short-circuit detection to current limiting and then to overtemperature detection.

This operating waveform was obtained by using a p-channel MOS as the load and gradually increasing the drain current from 0 A to verify operation of the F5041's protection function from short-circuit detection to current limiting and then to overtemperature detection. Figure 4 shows the short-circuit and overtemperature detection circuit. This detection circuit contains an internal resistor for monitoring the ON-voltage of the output-stage power MOSFET. A drain-source voltage monitoring circuit detects when the drain

Table 1 Fuji Electric's product line of smart MOSFETs

			Н	igh-s	ide											Low	-side													
	Type				IPS				IG BT						I	ntelli	gent	powe	r MC	SFE	Т									
	odel ımber	F5016H	F5017H	F5038H	F5044H	F5045P	F6008L	F5025	F5024	F5020	F5022	F5018	F5042	F5019	F5043	F5023	F5026	F5027	F5029	F5030	F5031	F5032	F5028	F5033	F5041	F5048				
		ТО	-220]	F-5	so	P-8	TO-220 K-Pack				T-P	ack	то-	220	T- Pack	TO- 220	T- Pack	K-Pack		T- Pack SOP-8		P-8	T- Pack							
			TO-2	220F-	5		K-	-Pack	(D-F	ack)		T-	Pack	$(D^2-I)$	Pack)				)-220					OP-8	_					
P	ackage		1	7	•			5											7		4									
ing	Voltage (V) *1 Current (A) *2	60	60	50	50	50	370	370	410	40	70	4	0		40		4	0	4	0	40	40	40	4	0	80				
Rat	Current (A) *2	3	6	3	3	1	8.5	8.5	10	3	3	3	3		12		5	2	1	.8	14	6	28	1 A (	2in1)	15				
re	ax. ON-state sistance, $_{\mathrm{DS(ON)}}\left( \Omega \right)$	0.16	0.16	0.16	0.12	0.6	$V_{ m sat}$ 1.3 V Typical value	$V_{ m sat}$ 1.3 V Typical value	$\begin{array}{c} V_{\rm sat} \\ 1.3~{\rm V} \\ \text{Typical} \\ \text{value} \end{array}$	0.4	0.55	0.14 0.14			0.14		0.02		0.02		0.02		0.	07	0.07	0.2	0.04	0.	.6	0.125
	Overcurrent detection	0	0	0	0	0	0	0		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0				
	Over- temperature detection	0	0	0	0	0	0			0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0				
c	Overvoltage detection	0	0	0	0																									
Function	Open load detection	0	0	0	0																									
돈	Status output	0	0	0	0																									
	Induced	0	0	0	0	0																								
	voltage clamping	(Typ valı -11	ıe	(Typ	oical ue -4	2 V)	Drain-gate clamping Zener diode										-													
	ow standby irrent					0													0											
Low noise (in output switching mode at time of overcurrent detection)  O O O												* 9		* 9																
K	emarks							*3 *3																						

<sup>\* 1 :</sup> Voltage limited by drain-gate Zener diode

Table 2 F5041 maximum ratings ( $T_i = 25^{\circ}$ C)

Item	Symbol	Rating	Measurement conditions	Unit
Drain-source voltage	$V_{ m DSS}$	40	DC	V
Gate-source voltage	$V_{ m GSS}$	-0.3 to +7.0	DC	V
Drain current	$I_{ m D}$	1		A
Max. power dissipation	$P_{ m D}$	1.5	See note below.	W
Junction temp.	$T_{ m j}$	150		$^{\circ}\mathrm{C}$
Storage temp.	$T_{ m stg}$	-55 to +150		$^{\circ}\mathrm{C}$

Note : When mounted on a 1,000  $\mbox{mm}^2$  glass epoxy substrate and 2 channels are ON simultaneously

current flowing to that resistor exceeds the shortcircuit current detection value, and in such a case, functions to limit the output current by lowing the value of the gate voltage of the output-stage power MOSFET to a specific voltage value. Moreover, if the continuation of this current-limiting operation causes the device's junction temperature  $(T_j)$  to rise above a certain value, an overtemperature detection circuit will operate to turn-off the output current.

The intelligent power MOSFET is designed for auto-restart upon returning from an operating sequence of short-circuit and overtemperature detection. Moreover, compared to the case where the temperature sensor is located next to the active part of the power

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<sup>\* 2 :</sup> Current limited by built-in protection circuit
\* 3 : Turn-off time (50 µs or less) is shorter than for F5018 and F5019

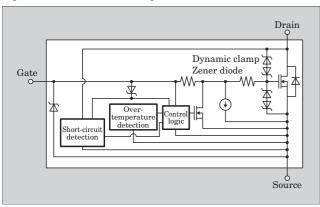
Table 3 F5041 electrical characteristics ( $T_i = 25^{\circ}C$ )

Item	Symbol	Measurement condition	va	dard lue Max.	Unit
Drain-source voltage	$V_{ m DSS}$	$I_{\mathrm{D}}$ = 1 mA, $V_{\mathrm{GS}}$ = 0 V	40	60	V
Gate threshold voltage	$V_{ m GS(th)}$	$I_{\mathrm{D}}$ = 1 mA, $V_{\mathrm{DS}}$ = 13 V	1.53	2.8	V
Drain current at zero gate	$I_{ m DSS}$	$V_{ m DS}$ = 16 V		15	μΑ
voltage		$V_{\mathrm{DS}} = 30 \; \mathrm{V}$		35	μΑ
Drain current at negative	$I_{ m DS(-VGS)}$	$V_{\rm DS} = 16 \text{ V} V_{\rm GS} = -1.5 \text{ V}$		12	μΑ
gate voltage	DB (=VGB)	$V_{\rm DS} = 30 \text{ V}$ $R_{\rm G} = 100 \Omega$		30	μΑ
C-4	I <sub>GS (n)</sub>	$V_{\rm GS}$ = 5 V (Note 1)		250	μΑ
Gate-source current	$I_{ m GS(un)}$	$V_{\mathrm{GS}}$ = 5 V, $T_{\mathrm{j}}$ >150°C (Note 2)		350	μΑ
ON-state resistance	$R_{ m DS(on)}$	$V_{\mathrm{GS}}$ = 5 V, $I_{\mathrm{D}}$ = 0.5 A		600	$m\Omega$
Overcurrent detection	$I_{ m OC}$	$V_{\mathrm{GS}}$ = 5 V	1.5		A
Over- temperature detection	$T_{ m trip}$	$V_{ m GS}$ = 5 V	150		$^{\circ}\mathrm{C}$
Switching	$t_{ m on}$	$V_{\rm DS} = 13 \text{ V}, I_{\rm D} = 0.5 \text{ A}$		50	$\mu s$
time	$t_{ m off}$	$V_{\rm GS} = 5 \text{ V}$		50	μs
Dynamic clamping energy dissipation	$E_{ m CL}$	$T_{ m j}=150^{\circ}{ m C}$	25		mJ

Note 1: Normal operation when the protection function is not active

Note 2: When the protection function is operating (in the load short circuit, overcurrent detection, or overtemperature detection modes)

Fig.1 F5041 circuit block diagram



MOSFET cell, the adoption of a layout in which the overtemperature detection sensor is positioned directly above the active part of the power MOSFET cell enables an overtemperature detection response speed that is approximately 10 times quicker and greater detection accuracy and enhanced protection functions to be obtained.

# (2) Dynamic clamping function

In automobile systems where there are many inductive loads such as solenoid valves, there is a

Fig.2 F5041 chip die photo

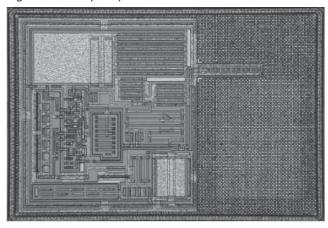


Fig.3 F5041 waveform at time of short-circuit detection, current limiting, and overtemperature detection

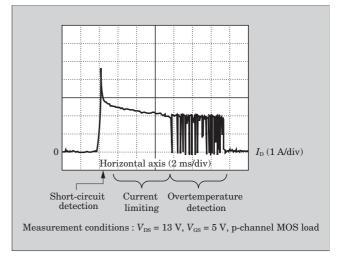
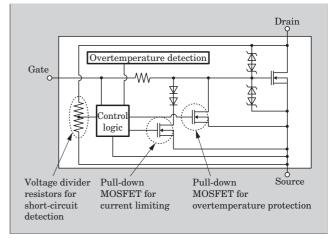


Fig.4 F5041 circuit for short-circuit detection, current limiting and overtemperature detection



problem of dealing with the  $LI^2/2$  energy accumulated in the inductive loads.

The intelligent power MOSFET contains a dynamic clamping circuit that clamps the surge voltage generated when an inductive load turns off and

absorbs the energy accumulated in the inductive load with the power MOSFET itself. This dynamic clamping circuit eliminates the need for external components such as a snubber circuit.

# (3) High ESD capability

The intelligent power MOSFET has been carefully designed to be capable of withstanding surge voltages in the harsh surge environment of automobiles. Specifically, the construction of the Zener diode for surge absorption and the circuit layout have been optimized and the operating resistance decreased to ensure that ESD capability between the drain and source is at least 25 kV (at 150 pF, 150  $\Omega$  and  $T_{\rm a} = 25^{\circ}{\rm C}$ ).

# 3. IPSs

# 3.1 Overview

Fuji Electric's line of IPS products is listed in Table 1. As a representative device from this product line, main specifications of the F5044H are listed in Tables 4, 5 and 6, and a circuit block diagram and chip die photo are shown in Figs. 5 and 6, respectively

### 3.2 Characteristics

# (1) Overcurrent protection

The IPS is equipped with an overcurrent protection function for protecting the system, load and device itself when an excessive current flows into the outputstage power MOSFET. As an example, Fig. 7 shows the operating waveform of the F5044H over the course of the sequence from overcurrent detection to the current switching mode. With the F5044H, the peak current value during the output switching mode is clamped at approximately 12 A (prior products had a peak current of 30 A). Even under abnormal conditions when the current flow is excessively large, the noise generated by the device during output switching is suppressed to a low value. Moreover, this reduction in peak current is advantageous for the trends toward use of thinner wiring for ECUs and thinner and lighter wire for wire harnesses.

# (2) Dynamic clamping function

As in the case of intelligent power MOSFETs, the handling of energy stored in an inductive load is also a problem for IPSs.

Similar to the intelligent power MOSFET, the IPS incorporates a dynamic clamping function that clamps the surge voltage generated when an inductive load turns off and absorbs the energy accumulated in the inductive load with a power MOSFET.

# (3) Low loss

In contrast to the conventional IPS fitted in a TO-220 full-mold 5-pin package (TO-220F-5), the F5044H is fitted in an SOP-8 package to achieve a more compact size. The largest problem encountered in making the package size smaller was in maintaining the conduction capacity and acceptable loss, but this was resolved by lowering the ON-state resistance to

Table 4 F5044H maximum ratings ( $T_i = 25^{\circ}$ C)

Item	Symbol	Rating	Measurement conditions	Unit
Supply voltage	$V_{ m CC}$	33/50	DC/0.25 s	V
Output current	$I_{ m OUT}$	3	Internally limited value	A
Input voltage	$V_{ m IN}$	$^{-0.3}$ to $V_{\mathrm{CC}}$ +0.3	DC	V
Status current	$I_{ m ST}$	5		mA
Operating junction temp.	$T_{ m j}$	150		°C
Storage temp. range	$T_{ m stg}$	-55 to +150		°C

Table 5 F5044H electrical characteristics ( $T_i = 25^{\circ}$ C)

			,		
Item	Symbol	Measurement	Standar	d value	Unit
Item	Symbol	conditions	Min.	Max.	Cint
Operating voltage	$V_{ m CC}$		6	28	V
Standby current	$I_{ m CC}$	$\begin{aligned} V_{\mathrm{CC}} &= 13 \; \mathrm{V} \\ R_{\mathrm{L}} &= 10 \; \Omega \\ V_{\mathrm{IN}} &= 0 \; \mathrm{V} \end{aligned}$		3	mA
Input	$V_{ m IN(H)}$	$V_{\mathrm{CC}}$ = 13 V	3.5		V
voltage	$V_{ m IN(L)}$	$V_{\rm CC}$ = 13 V		1.5	V
Input current	$I_{ m IN(H)}$	$V_{\mathrm{CC}}$ = 13 V $V_{\mathrm{IN}}$ = 5 V		12	μΑ
ON-state resistance	$R_{ m DS(on)}$	$\begin{aligned} V_{\mathrm{CC}} &= 13 \; \mathrm{V} \\ I_{\mathrm{out}} &= 1.25 \; \mathrm{A} \end{aligned}$		0.12	Ω
Overcurrent detection	$I_{ m OC}$	$V_{ m CC}$ = 13 V	3	6	A
Over- temperature detection	$T_{ m trip}$	$V_{\rm CC}$ = 13 V	150	200	°C
Overvoltage detection	$V_{ m OV}$		28	33	V
Turn-on time/ turn- off time	$t_{ m on}$ / $t_{ m off}$	$V_{\rm CC}$ = 13 V $R_{\rm L}$ = 10 $\Omega$		120/40	μs
Output clamp voltage	$V_{ m clamp}$	$V_{\rm CC}$ = 13 V L = 10 mH	$-(50-V_{\rm CC})$	$-(60-V_{\rm CC})$	V
Open load detection	$R_{ m LOPEN}$	$V_{\mathrm{CC}} = 13 \ \mathrm{V}$ $V_{\mathrm{IN}} = 0 \ \mathrm{V}$	6	36	kΩ

Table 6 F5044H logic table

Item	IN	ST	OUT	Remark
Normal operation	L H Open	L H L	L H L	
Open load	L	Н	Н	Auto-restart
Overcurrent	L H	L L	L L	Switching mode Auto-restart
Overtemperature	L H	L L	L L	Auto-restart
Overvoltage	L H	L H	L L	Auto-restart

 $120~\text{m}\Omega$  (max.). Figure 8 compares the mounting area and acceptable conduction capacity of the TO-220F-5 and SOP-8 package IPSs.

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Fig.5 F5044H circuit block diagram

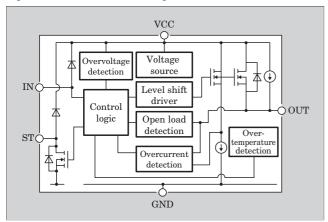


Fig.6 F5044H chip die photo

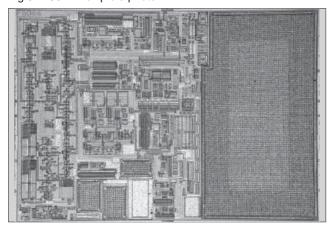
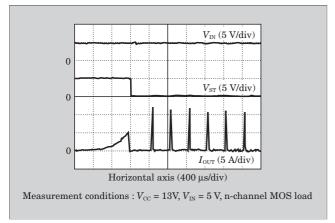


Fig.7 F5044H waveform during sequence from overcurrent detection to output switching mode



# 4. Self-isolation Technology

In the case of a device such as a smart MOSFET that integrates a vertical power MOSFET and control IC into a single chip, isolation of the structures is important. Fuji Electric uses self-isolation CMOS/DMOS (complementary MOS/diffusion MOS) technology in its line of smart MOSFET products. Figure 9

Fig.8 Comparison of mounting area and acceptable conduction capacity of the TO-220F-5 package IPS and SOP-8 package IPS

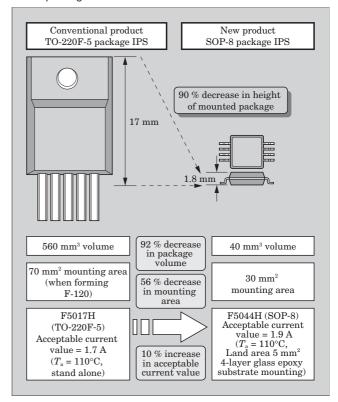
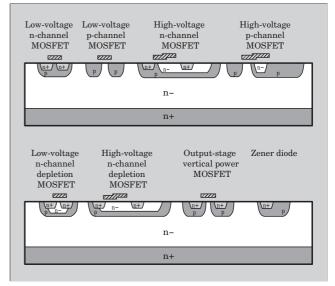


Fig.9 Cross-section of self-isolation structure (IPS)



shows a cross-section of the IPS series as a representative example of the smart MOSFET product line. Fabricated on the same silicon substrate as the power MOSFET, the self-isolated structures consist of low and high-voltage CMOS devices, a Zener diode and the like separated by each device's own p-n junction and integrated together with the power MOSFET. This self-isolation technology can realize low cost structures since it requires fewer processes than junction-isola-

Requested features of Requested features of Response by Fuji Electric's smart devices automotive electrical equipment semiconductor devices Intelligent power MOSFET Built-in protection function Overcurrent, overtemperature, overvoltage protection function Built-in peripheral circuitry Input pull-down, broken load wire detection function Integration Integrated intelligent devices More compact size Super compact, smart MOS Compact surface mount package Fewer components Self-isolation technology Low cost wafer process technology Next generation SI process Less required ECU design work Chip-on-chip smart MOS Larger current Ability to withstand higher voltages Lower cost High reliability, long life High surge resistance Vehicle status monitor Low standby current Compatibility with operation in Low noise high temperature environment Status output to CPU Low operating voltage Highly accurate current detection Support of serial transfers Integrated intelligent devices Compatibility with operation in Products compatible with a high temperature environment 175°C operating environment

Fig.10 Requested features of semiconductor devices for the automotive electrical equipment market and the response by Fuji Electric's smart devices

tion technology or silicon-on-insulator technology, and the silicon wafer does not require special processing. Moreover, by making full use of self-isolation CMOS/DMOS technology based on a vertical power MOSFET process, commercialization can be achieved by adding approximately 3 to 6 mask steps and processes.

# 5. Conclusion

Figure 10 shows the features requested of semiconductor devices for the automotive electrical equipment market and the conformance to those requests by smart devices. Fuji Electric has responded to the customer and marketplace requests listed in Fig. 10 by introducing intelligent power MOSFETs and IPSs in its new line of smart MOSFET products. In the future, Fuji Electric intends to develop integrated devices such

as ICs equipped with a surge absorption function for applications that require the integration of systems and circuits, chip-on-chip smart MOSFETs for applications that require small-size power devices having a large current capacity, and super-small smart MOS FETs for applications in which further miniaturization of 1-channel smart MOSFETs is required. Additionally, as a wafer process, we are developing next generation self-isolation technology that integrates lateral power devices and control ICs in order to achieve multi-channel capability. While continuing to promote the above-described technology and product development to leverage the advantages of conventional smart MOSFETs, Fuji Electric intends to contribute to making ECUs more compact in size and to achieving overall cost reductions.

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# A Self-isolated Single-chip Igniter (F6008L) for Automobiles

Mitsutoshi Yamamoto Kenichi Ishii Yoshiaki Toyoda

# 1. Introduction

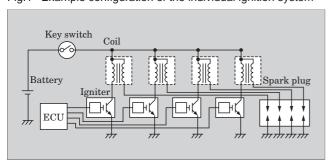
Driving by concerns for preventing atmospheric pollution and global warming caused by gas emissions, the automobile industry has, in recent years, aggressively promoted the development of vehicles capable of achieving dramatically lower fuel consumption and reducing the amount of hazardous substances in gas emissions. These capabilities are also in demand for automotive parts.

Regarding the ignition control sub-assembly of a gasoline engine control system, there are also strong demands for more stable ignition coil voltage and more precise control in order to achieve higher fuel efficiency and lower gas emissions. Consequently, instead of the conventional distributor method in which a mechanical mechanism is used to distribute a high voltage to an ignition spark plug for each cylinder, the use of an individual ignition method has increased in popularity in recent years. In the individual ignition method, a coil and switch are provided for each ignition spark plug and the ignition interval is adjusted according to the operational timing of each cylinder. Figure 1 shows an example block diagram of an individual ignition system for each cylinder.

In response to requests for higher performance, Fuji Electric commercialized the world's first single-chip igniter (F5025) that incorporated an IGBT having a self-isolation structure, and has been mass-producing this device since 1998.

This paper introduces the newly developed F6008L, which adds an overtemperature protection

Fig.1 Example configuration of the individual ignition system



function to Fuji Electric's popular F5025 product line of single-chip igniters.

# 2. Overview

An igniter device is used in the central part of the engine drive system and because device failure would likely cause the engine to stop operating, extremely high

Fig.2 F6008L schematic diagram

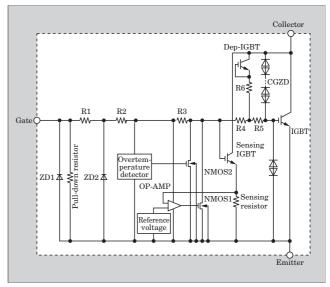
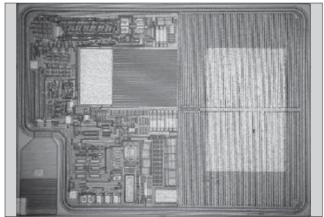


Fig.3 F6008L chip die photo



reliability is required. The F6008L has the following features and capabilities to satisfy such requirements.

- (1) The F6008L achieves high system reliability by incorporating current-limiting and overtemperature protection functions.
- (2) The F6008L realizes the same high surge withstand capability, high inductive load protection, high electromagnetic noise immunity and high resistance to adverse environmental conditions as the F5025, and additionally is provided with an overtemperature protection function.
- (3) The F6008L integrates an igniter device and overtemperature protection function into the same TO-220 compact package as used to house the F5025.

A block diagram of the internal circuitry of the F6008L is shown in Fig. 2 and a chip die photograph is shown in Fig. 3.

# 3. Characteristics and Functions

Main electrical specifications and specific features of the F6008L are described below.

# 3.1 Electrical characteristics

Table 1 lists main electrical characteristics of the F5025 and F6008L. Electrical characteristics of the newly developed F6008L basically inherit those of the F5025, allowing easy device substitution without requiring additional circuit modification.

The collector-emitter voltage is determined by the Zener diode connected between the collector and gate, and the gate-emitter voltage is determined by the value of the surge protection Zener diode (ZD1). Because a current detection resistor is not used in the emitter line, the collector-emitter saturation voltage is low as same as the usual IGBT saturation voltage.

# 3.2 Overtemperature protection function

If a gate signal is input to the igniter device for a

longer than usual duration, because the igniter load is a coil, the load current will reach an overcurrent state and device temperature will rise. In this case, the igniter device will activate its current-limiting function that is able to self-protect the device for a certain period of time. During normal operation, the duration of the gate signal input does not extend for a longer period of time than a certain presumed duration. But, for whatever reason, in the case where the duration of the ON signal exceeds this duration interval or the ambient temperature rises to an abnormal level or the like, the chip will generate an abnormal amount of heat that exceeds the design temperature, and this generated heat may cause damage to the chip.

For cases such as the above when an abnormally high temperature is reached, the F6008L is provided with an overtemperature protection function that operates to protect the chip from heat damage by forcibly turning off the IGBT collector current when the chip temperature reaches a certain value.

Figure 2 shows a schematic diagram of the overtemperature protection circuitry that is built into the chip. The F6008L integrates an IGBT power circuit and a control circuit into a single chip. An overtemperature detection unit built into the IGBT power circuit and a decision unit provided in the control circuit operate to detect and determine the temperature of the chip, and when a specific temperature is reached, the IGBT's gate is pulled-down to cutoff the collector current.

Features of the F6008L's overtemperature protection function are described below.

(1) High precision temperature detection using trimming technology

The F6008L uses Zener-zap trimming technology to suppress fluctuation in the overtemperature detection threshold temperature. Because the F6008L has overtemperature sensor and detection functions built into a single chip, temperature trimming can be

Tabla 1	Flactrical	characteristics	of the	F5025 and	FROORI

Tr.	C1 -1	Measurement	F5025		F6008L	
Item	Symbol	condition	Min.	Max.	Min.	Max.
Collector breakdown voltage	$V_{ m CE}$	I <sub>C</sub> = 10 mA *	370 V	460 V	Same a	s at left
Gate breakdown voltage	$V_{ m GE}$	$I_{\mathrm{GE}}$ = 10 mA	6 V	10 V	Same a	s at left
Limiting current	$I_{ m CL}$	$V_{\rm GE} = 3.5 \ { m V} * \ V_{\rm CE} = 5 \ { m V}$	8.5 V		Same a	s at left
Collector-emitter saturation voltage	V <sub>CE (sat)</sub>	$V_{\rm GE} = 3.5 \ { m V} * \ I_{ m C} = 6 \ { m A}$		1.7 V	Same a	s at left
Gate-emitter threshold voltage	$V_{ m CE(th)}$	$V_{\rm CE}$ = 16 V * $I_{\rm C}$ = 3 mA	0.7 V	_	Same a	s at left
Gate leakage current	$I_{\mathrm{CES}}$	$V_{\rm CE}$ = 300 V		500 μΑ	Same a	s at left
Gate pull-down current	$I_{ m GES}$	$V_{\rm GE}$ = 3.5 V *	2 mA	3.5 mA	2 mA	4 mA
Turnoff time	$T_{ m d} \ T_{ m f}$	$V_{\rm GE} = 3.5 \ { m V} * \ I_{ m C} = 6 \ { m A}$	_	35 μs 15 μs	Same a	s at left
Overtemperature detection	$T_{ m trip}$		_	_	175°C	205°C

<sup>\*</sup>  $T_i = -40 \text{ to } +150^{\circ}\text{C} \text{ (otherwise } T_i = 25^{\circ}\text{C)}$ 

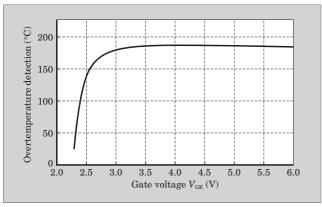
implemented by directly comparing fluctuations of both these characteristics. In the wafer probing process, both characteristics are compared and a selector is used to select the optimal detection value and correct the detection temperature. This technology has enabled the realization of high precision temperature trimming with an NMOS circuit.

(2) Realization of an overtemperature protection function using a 3-pin configuration

The F6008L is constructed such that the control circuit is provided with power via the gate input pin, and this enables overtemperature protection to be realized with the same 3-pin configuration as a standalone IGBT or F5025, without requiring the provision of an additional power supply pin, thereby achieving package interchangeability with the F5035. However, fluctuation in the characteristics of the overtemperature detection circuit (detection temperature fluctuation) due to fluctuation of the gate voltage (fluctuation of the power supply voltage for the control circuit) is a problem. The voltage of the gate signal from an electronic control unit (ECU) is typically in the range of 4.0 to 5.0 V, but an igniter device must be designed with the assumption that gate voltage will be lowered to 3.0 V or less. For an overvoltage detection circuit constructed from NMOS circuitry, the constituent circuitry will have a large fluctuation in detection temperature characteristics in this range of power supply voltage, and accordingly, the detection temperature will have a large dependency on gate voltage. To address this problem, the F6008L adds a circuit to correct for the fluctuation in characteristics caused by gate voltage, thereby making the detection temperature less dependent on gate voltage. Furthermore, so that the overtemperature detection threshold temperature does not rise above the maximum value of 205°C while the gate voltage is low, the F6008L has been designed such that its overtemperature detection threshold temperature will decrease together with the decrease in gate voltage.

These measures have dramatically decreased the fluctuation of detection temperature due to gate voltage fluctuation and have realized a mechanism that

Fig.4 Overtemperature characteristics



safely halts igniter operation when the gate voltage decreases. Figure 4 shows the gate voltage (circuit voltage) dependency of the overtemperature detection threshold temperature.

# 3.3 Current-limiting function

Because the igniter has a coil as its load, if the ON signal from the ECU continues for a long time, the collector current will increase up to the value determined by the battery voltage and the circuit inductance and resistance, and in the worst case scenario, the igniter will be damaged by an overcurrent. To prevent this from happening, it is critically important to add a current-limiting function.

Fuji Electric's single-chip igniter utilizes a current detection and limiting method based on sensing IGBT technology. Because this method does not require a current-detecting shunt resistor to be connected directly in series with the main IGBT, there is no voltage drop due to the flow of collector current through a shunt resistor, and therefore  $V_{\rm CE(sat)}$  can be reduced.

Also, to prevent the surge in collector-emitter voltage generated at the start of the current-limiting operation (which would generate an unnecessary voltage at the secondary coil and could potentially cause

Fig.5 F6008L operating waveforms

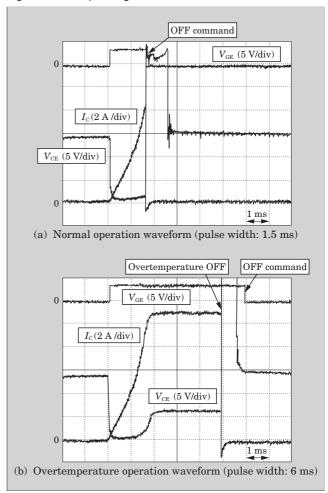
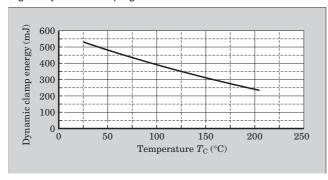


Fig.6 Dynamic clamping characteristics



unintentional ignition sparking), our single-chip igniters also utilize Fuji Electric's proprietary technology for preventing oscillation during current limiting, thereby resolving a problem which had been difficult to handle with only IGBT-based technology.

Waveforms during normal operation and in the case where current-limiting and overtemperature detection functions are active are shown in Fig. 5(a) and 5(b), respectively. Waveforms are shown assuming operation in the two cases where an ON signal is input for the usual ON duration (approximately 2 ms) and for a longer-than-usual ON duration (approximately 6 ms, where the ambient temperature was 170°C so that overtemperature detection would be easy to activate). It can be seen that the current-limiting function is not activated for the usual ON duration, but in the case of an extended ON duration, after current-limiting is activated, the overtemperature protection function acts to turn off the current even before an OFF signal is input.

# 3.4 Dynamic clamping

If the igniter device misfires, the device must be able to process the inductive load energy stored in the ignition coil. The amount of such energy is usually in the range of several tens of mJ to  $100\,\mathrm{mJ}$ . Since the F6008L is specified for overtemperature detection in the range from  $175^\circ\mathrm{C}$  to  $205^\circ\mathrm{C}$ , a dynamic clamping capability of  $100\,\mathrm{mJ}$  is guaranteed at the overtemperature detection threshold temperature.

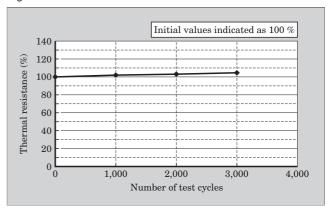
Figure 6 shows typical dynamic clamping characteristics of the F6008L. It can be seen that the F6008L has sufficient capability even at the maximum overtemperature detection threshold temperature of  $205^{\circ}$ C.

# 3.5 Electromagnetic noise immunity

Electromagnetic noise immunity of the F6008L was verified using the TEM-cell method. We verified that in an electric field of 200 V/m and frequency range of 10 MHz to 1 GHz, there were no operational anomalies during current-limiting, overtemperature detection or ON-OFF switching operation.

Also, based on the assumption that noise will be input from the ignition coil or elsewhere to the gate pin, we performed a test in which an ESD (electrostatic

Fig.7 Thermal shock test results



discharge) surge (150 pF, 150  $\Omega$ , 5 to 25 kV) was applied between the gate and emitter pins as noise, and then verified that there was no anomaly in the operation of the overtemperature detection circuit under these conditions.

# 3.6 Resistance to environmental conditions

There is demand for igniter systems to be made smaller in size or packaged together with a coil in order to eliminate the high-tension lead between a coil and igniter device, and Fuji Electric is considering an integrated package as it continues to develop singlechip igniter technology. In the case of integration with a coil, because the resultant device will be mounted directly on an engine, it must be highly resistant to adverse environmental conditions in order to withstand the extreme temperature fluctuations in an engine compartment. Fuji Electric's single-chip igniter uses low-stress high-density resin and stress-resistant solder. As a result, after 3,000 cycles of a thermal shock test (-55 to +150°C,  $\Delta T_{\rm C}$  = 205 K) the change in thermal resistance, which is an indicator of deterioration, was suppressed to 5% or less, thereby verifying the strong resistance to environmental conditions. Figure 7 shows a graph of the thermal shock test cycle and change in thermal resistance.

# 4. Conclusion

Similar to the requirements automotive systems, ignition systems are also expected to require smaller size, greater functionality, higher performance and higher reliability in the future. In response to those requirements, Fuji Electric intends to promote the development of new small-size, multi-function, single-chip igniter products as successors to the F5025 and F6008L.

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# **Automotive Pressure Sensors**

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### 1. Introduction

As the automotive industry moves to comply with global environmental regulations in Europe, North America, Asia and elsewhere, the industry is promoting efforts to boost the efficiency and to achieve higher control accuracy of engine systems. For the control of gasoline engines and diesel engines, a higher degree of accuracy is being required in pressure sensors in order to accurately monitor (measure) conditions such as the air volume and the exhaust gas pressure of the EGR (exhaust gas recirculation) system and to increase efficiency. Moreover, due to an increase in pressure sensor applications, such as the use of a barometric pressure sensor to perform altitude correction when driving at high-altitudes, automotive-use pressure sensors are required to have high accuracy and a low price.

In response to these requirements, Fuji Electric has developed an automotive pressure sensor with digital trimming that is fabricated using a CMOS (complementary MOS) process. Product development was based on the concept of providing an "all in single chip solution" and the commercialization of products

was promoted with the goal of realizing the lowest possible product failure rate at low cost. This paper introduces Fuji Electric's product lineup and future outlook for automotive pressure sensors.

# 2. Special Features

Figure 1 shows the technical trends of pressure sensor cell in Fuji Electric's automotive pressure sensor cells. Fuji Electric's first generation of massproduced automotive pressure sensors in 1984 used pressure sensor chips equipped with only a gauge function, and other functions such as an amplifier circuit, trimming resistor and EMI filter were provided by packaging the sensor together with a hybrid IC. Subsequently, as of the second generation, a thin film trimming resistor for trimming was built-in to the chip. In the newly developed pressure sensor as the third generation, a vacuum cavity is fabricated by means of anodic bonding of glass and silicon, and the device construction consists of connection terminals and a resin package housing a sensor chip and its built-in functions only. The material for the resin package was selected based on considerations such as

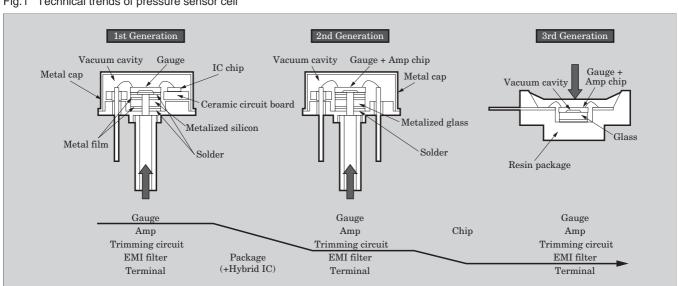


Fig.1 Technical trends of pressure sensor cell

adhesion to the connection terminals and temperature stability.

# 2.1 Pressure sensor chip

The pressure sensor chip developed by Fuji Electric is shown in Fig. 2. This chip was realized using Fuji Electric's proprietary MEMS (micro-electronics and mechanical system) technology and is provided with the following functions.

- 1) A function for converting pressure into strain
- 2) A function for providing a vacuum cavity
- 3) A function for converting a change in resistance into an electrical signal

Fig.2 Pressure sensor chip

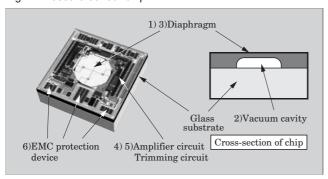


Table 1 Absolute maximum ratings

Item	Symbol	Unit	Standard specification	
Overvoltage	$V_{ m max}$	V	<16.5 V	
Storage temperature	$T_{ m sto}$	$^{\circ}\mathrm{C}$	-40 to +135	
Proof pressure	$P_{\mathrm{max}}$	%F.S.	200	
Burst pressure	$P_{ m burst}$	%F.S.	300	
International EMC standards	JASO D00-87, CISPR25, ISO11452-2, ISO7637			

Table 2 Standard specifications

Item	Symbol	Unit	Standard specification
Operating voltage	$V_{ m cc}$	V	5±0.25
Operating current	$I_{\rm cc}$	mA	<10
Operating temperature	$T_{ m op}$	°C	-40 to +135
Output voltage	$V_{ m out}$	V	0.5 to 4.5
	$P_{\mathrm{op}1}$	kPa	10 to 120
Measurement	$P_{ m op2}$	kPa	20 to 250
pressure range*1	$P_{ m op3}$	kPa	50 to 300
	$P_{ m op4}$	MPa	up to 20 *2
Sink current	$I_{ m sink}$	mA	1
Source current	$I_{ m source}$	mA	0.1
Pressure error	$V_{ m per}$	%F.S.	<1.0
Temperature error	$V_{ m ter}$	%F.S.	<1.5

<sup>\*1:</sup> The pressure range can be set to an arbitrary value with the diaphragm thickness.

- 4) A function for amplifying electrical signals
- 5) A function for adjusting electrical signals to specific characteristic values and then maintaining that adjustment
- 6) A function for protecting electrical signals from external noise

In particular, compared to a conventional bipolar process, the use of a CMOS process enables this pressure sensor chip to achieve a higher degree of EMC protection (such as overvoltage, ESD, EMI, and surge protection), as is required of automotive-use devices.

Figure 2 shows the pressure sensor chip developed by Fuji Electric. A diaphragm that realizes the abovementioned functions 1) and 3) is formed in the center of the silicon chip. Also, technology for anodic bonding to the glass substrate provides the abovementioned function 2), and ensures high reliability by maintaining a high vacuum condition for an extended period of time. Moreover, an amplifier circuit and trimming circuit for supplying functions 4) and 5) are provided at the periphery of the diaphragm. The absolute maximum ratings and standard specifications for a pressure sensor that uses this chip are shown in Tables 1 and 2, respectively.

# 2.2 Concept of the product lineup

Fuji Electric's automotive pressure sensors are based on the concepts shown in Fig. 3 and this product

Fig.3 Concept of pressure sensors

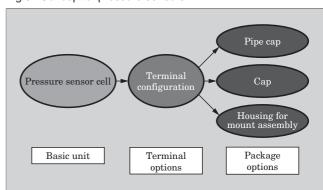


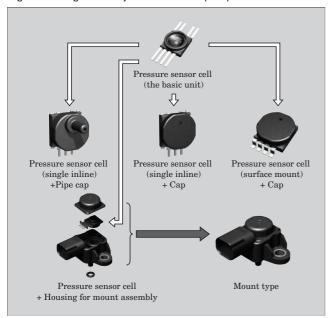
Table 3 Examples of automotive pressure sensor applications

	Application	Pressure range	Remark
	Manifold pressure	120 kPa	
	Turbocharged pressure	250 kPa	
Engine	gine Diesel		Commercial
	EGR	250 kPa	production
	Barometric pressure	120 kPa	
Air	R134a	5 MPa	Under
conditioner	$CO_2$	20 MPa	development
0.1	Brake system	5 MPa	Under
Oil actuator	Power steering		development
CVT		10 MPa	Under development

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<sup>\*2 : 20</sup> MPa high-pressure products are presently under development.

Fig.4 Packages for Fuji Electric's lineup of pressure sensors



lineup is configured from the combination of a pressure sensor cell, terminal configuration and package option. Table 3 lists example applications of automotive pressure sensors.

# (1) Pressure sensor cell

A pressure sensor cell houses the pressure sensor chip and provides the capability for outputting sensor signals from the pressure sensor chip to the exterior. The pressure sensor cell is the most basic unit in Fuji Electric's pressure sensor products. The package material was selected based on assumed usage in such automotive applications as the measurement of intake manifold suction, EGR exhaust gas pressure and the like, and chemical compatibility with materials such as gasoline, diesel gasoline, lubricant and the like. This pressure sensor cell forms the basis of Fuji Electric's

standard package lineup of pressure sensor products. Even among products of different terminal configurations, final package shapes and pressures ranges, because all pressure sensor cells are manufactured on the same production line, a significant reduction in production cost is achieved.

# (2) Terminal configuration

The terminal configuration of the pressure sensor cell can be selected to support a particular application in which the pressure sensor chip will be mounted. Figure 4 shows examples of Fuji Electric's standard specifications. Single inline, surface mount and other types of terminal configurations can be supported.

# (3) Package options

In response to various requests from customers, the pressure sensor cell package supports the attachment of hardware for the mechanical interface (pipe, cap, mount type) to a particular application. Figure 4 shows Fuji Electric's standard product series. The pipe and cap type are examples of applications in which the pressure sensor will be mounted on a printed circuit board, and the mount type is an example suitable for installation on an engine. A new high-voltage package is currently under development.

# 3. Conclusion

This paper has described the product concept behind Fuji Electric's automotive pressure sensor products and introduced the product lineup that has been developed.

Environmental and safety regulations of various countries throughout the world are expected to lead to increasingly severe requirements for the accuracy, quality and price of automotive pressure sensors in the future, and Fuji Electric remains committed to the development of world-class automotive pressure sensor technology and products.



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